TD1F THRU TD10F

Surface Mount Glass passivated Bridge Rectifier Reverse Voltage - 100 to 1000 V Forward Current - 1 A

Features

- · Glass passivated chip
- · High surge current capability
- Designed for surface mount application

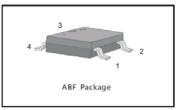
Mechanical Data

• Package: ABF

• Terminals: Solderable per MIL-STD-750, Method 2026

PINNING

PIN	DESCRIPTION						
1	Input Pin (~)						
2	Input Pin (~)						
3	Output Anode (+)						
4	Output Cathode (-)						



Maximum Ratings and Electrical characteristics

Single-phase, half-wave, 60 Hz, resistive or inductive load rating at 25°C, unless otherwise specified, for capacitive load, derate current by 20 %.

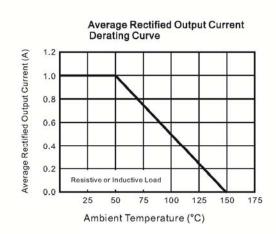
	Symbols	TD1F	TD2F	TD4F	TD6F	TD8F	TD10F	Units
Parameter	Marking	10F1	10F2	10F4	10F6	10F8	10F10	-
Maximum Repetitive Peak Reverse Voltage	V_{RRM}	100	200	400	600	800	1000	V
Maximum RMS voltage	V_{RMS}	70	140	280	420	560	700	٧
Maximum DC Blocking Voltage	V_{DC}	100	200	400	600	800	1000	٧
Average Forward Current at T _a = 50°C	I _{F(AV)}	1						Α
Peak Forward Surge Current 8.3 ms Single Half Sine Wave Superimposed on Rated Load(JEDEC Method)	I _{FSM}	35						Α
Peak Forward Surge Current 1 ms Single Half-square wave Superimposed on Rated Load	I _{FSM}	70						Α
Maximum Instantaneous Forward Voltage at 1 A	V _F	1.1						V
	I _R	5 50						μΑ
Typical Junction Capacitance 1)	C _j	13						pF
Typical Thermal Resistance ²⁾	$R_{ hetaJA} \ R_{ hetaJL}$	80 20						°C/W
Operating and Storage Temperature Range	T_j , T_{stg}	- 55 to + 150						ů

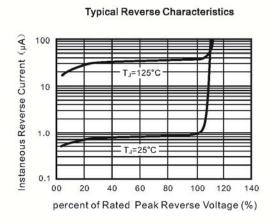
 $^{^{1)}\}mbox{Measured}$ at 1 MHz and applied reverse voltage of 4 V D.C.

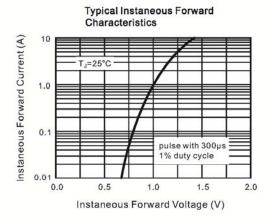


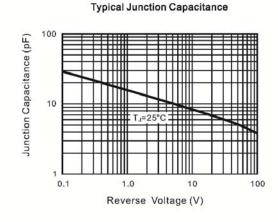
²⁾ Mounted on glass epoxy PC board with 4 X (5 X 5 mm²) copper pad.

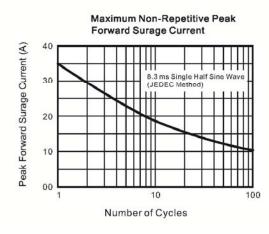
TD1F THRU TD10F







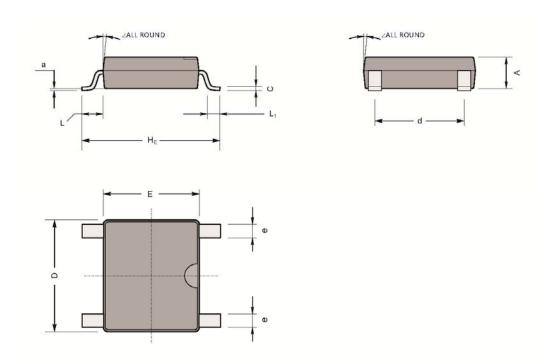






PACKAGE OUTLINE ABF

Plastic surface mounted package; 4 leads



UNIT	Α	С	D	Е	H _E	d	е	L	L1	а	
mm	1.2	0.22	5.2	4.5	6.4	4.2	0.7	0.95	0.6	0.1	7 °
	1	0.15	4.9	4.2	6	3.6	0.5				

Recommended Soldering Footprint

