MU10N280L

N-Channel Enhancement Mode MOSFET

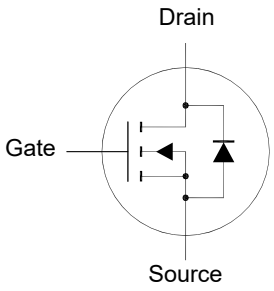
Features

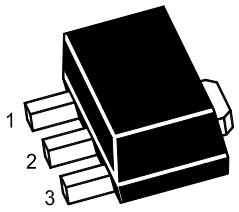
• Low Leakage Current

Applications

• Portable appliances

• Battery management





1.Gate 2.Drain 3.Source
SOT-89 Plastic Package

Absolute Maximum Ratings (at T_a = 25℃ unless otherwise specified)

Parameter	Symbol	Value	Unit
Drain Source Voltage	V_{DS}	100	V
Gate Source Voltage	V_{GS}	± 20	V
Drain Current	I_D	1.5	A
Peak Drain Current, Pulsed [1)]	I_{DM}	8	A
Power Dissipation [2)]	P_D	1.5	W
Operating Junction Temperature	T_J	150	℃
Storage Temperature Range	T_{stg}	- 55 to + 150	℃

Thermal Resistance Ratings

Parameter	Symbol	Max.	Unit
Thermal Resistance from Junction to Ambient [2)]	$R_{\theta JA}$	83.3	℃/W

[1)] Pulse Test: Pulse Width ≤ 100 μs, Duty Cycle ≤ 2%,Repetitive rating, pulse width limited by junction temperature $T_{J(MAX)}$=150°C.

[2)] Device mounted on FR-4 substrate PC board, 2oz copper, with 1-inch square copper plate in still air.

Dated: 21/07/2023 Rev:02

Characteristics at T_a = 25℃ unless otherwise specified

Parameter	Symbol	Min.	Typ.	Max.	Unit
STATIC PARAMETERS					
Drain-Source Breakdown Voltage at I_D = 250 µA	$V_{(BR)DSS}$	100	-	-	V
Zero Gate Voltage Drain Current at V_{DS} = 80 V	I_{DSS}	-	-	1	µA
Gate-Source Leakage at V_{GS} = ± 20 V	I_{GSS}	-	-	± 100	nA
Gate-Source Threshold Voltage at V_{GS} = V_{DS}, I_D = 250 µA	$V_{GS(th)}$	1	-	2.5	V
Drain-Source On-State Resistance at V_{GS} = 10 V, I_D = 2 A at V_{GS} = 4.5 V, I_D = 1 A	$R_{DS(on)}$	- -	- -	280 310	mΩ
DYNAMIC PARAMETERS					
Forward Transconductance at V_{DS} = 5 V, I_D = 1 A	g_{Fs}	-	3.5	-	S
Gate resistance at V_{DS} = 0 V, V_{GS} = 0 V, f = 1 MHz	R_g	-	1	-	Ω
Input Capacitance at V_{DS} = 50 V, V_{GS} = 0 V, f = 1 MHz	C_{iss}	-	466	-	pF
Output Capacitance at V_{DS} = 50 V, V_{GS} = 0 V, f = 1 MHz	C_{oss}	-	17.5	-	pF
Reverse Transfer Capacitance at V_{DS} = 50 V, V_{GS} = 0 V, f = 1 MHz	C_{rss}	-	13.5	-	pF
Total Gate Charge at V_{DS} = 50 V, I_D = 2 A, V_{GS} = 10 V at V_{DS} = 50 V, I_D = 2 A, V_{GS} = 4.5 V	Q_g	- -	8.6 4	- -	nC
Gate-Source Charge at V_{DS} = 50 V, I_D = 2 A, V_{GS} = 10 V	Q_{gs}	-	1.8	-	nC
Gate-Drain Charge at V_{DS} = 50 V, I_D = 2 A, V_{GS} = 10 V	Q_{gd}	-	1	-	nC
Turn-On Delay Time at V_{DS} = 50 V, I_D = 2 A, V_{GS} = 10 V, R_G = 6 Ω	$t_{d(on)}$	-	9.5	-	ns
Turn-On Rise Time at V_{DS} = 50 V, I_D = 2 A, V_{GS} = 10 V, R_G = 6 Ω	t_r	-	3.1	-	ns
Turn-Off Delay Time at V_{DS} = 50 V, I_D = 2 A, V_{GS} = 10 V, R_G = 6 Ω	$t_{d(off)}$	-	8.4	-	ns
Turn-Off Fall Time at V_{DS} = 50 V, I_D = 2 A, V_{GS} = 10 V, R_G = 6 Ω	t_f	-	12.7	-	ns
Body-Diode PARAMETERS					
Drain-Source Diode Forward Voltage at V_{GS} = 0 V, I_S = 1 A	V_{SD}	-	-	1.2	V
Body-Diode Continuous Current	I_S	-	-	1.5	A
Body Diode Reverse Recovery Time at I_S = 2 A, di/dt = 100 A / µs	t_{rr}	-	16	-	ns
Body Diode Reverse Recovery Charge at I_S = 2 A, di/dt = 100 A / µs	Q_{rr}	-	12.8	-	nC

Electrical Characteristics Curves

Fig. 1 Typical Output Characteristics

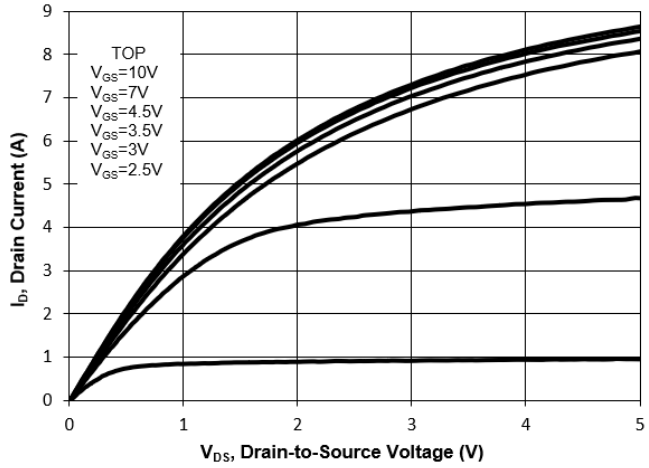


Fig. 2 Typical Transfer Characteristics

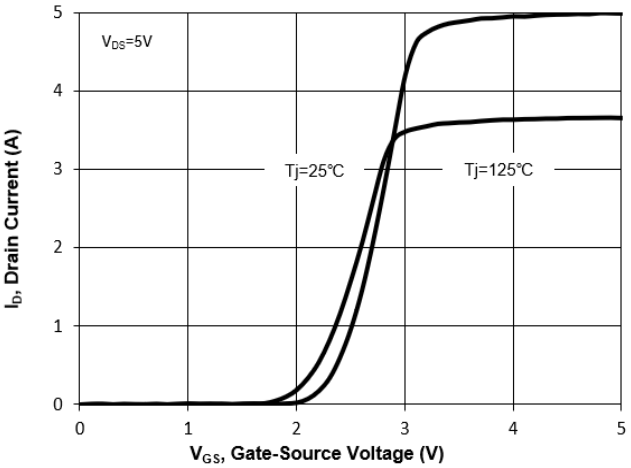


Fig. 3 on-Resistance vs. Drain Current

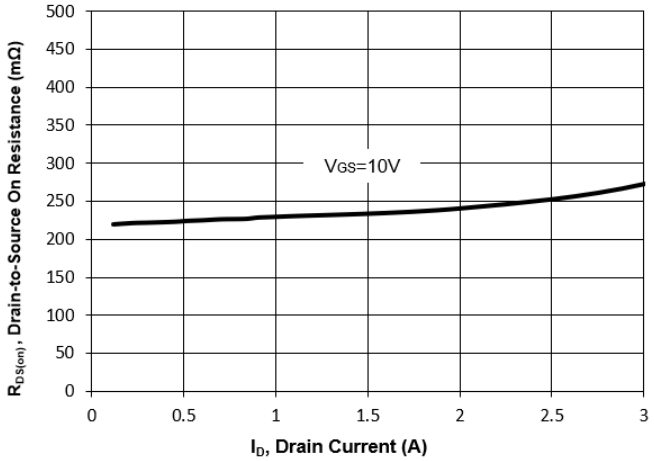


Fig. 4 On-Resistance vs. Gate-Source Voltage

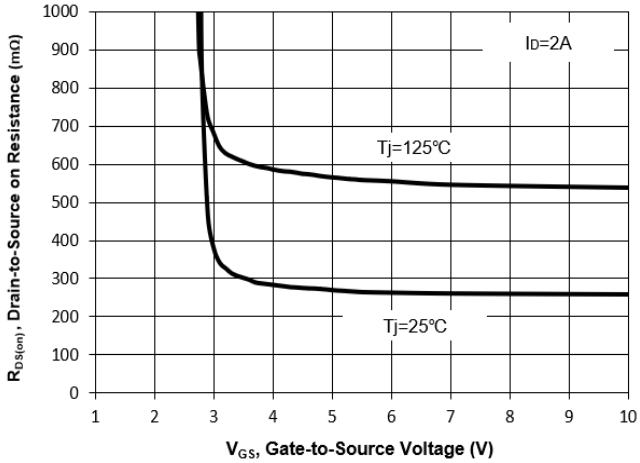


Fig. 5 on-Resistance vs.T_j

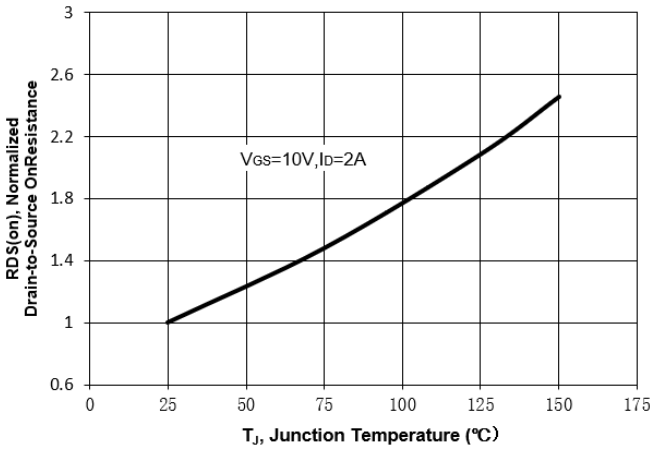
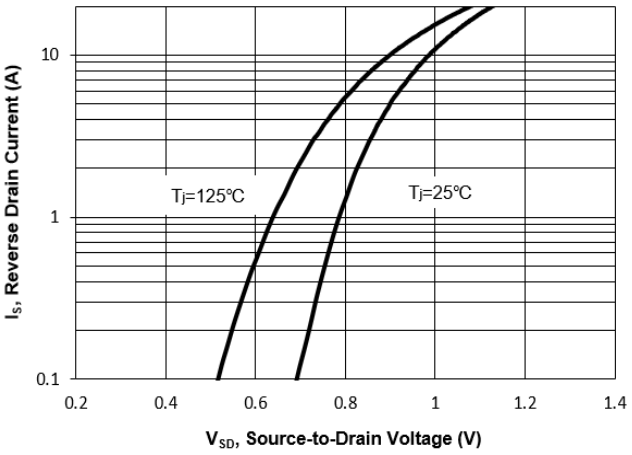


Fig. 6 Typical Forward Characteristics



Electrical Characteristics Curves

Fig. 7 Typical Junction Capacitance

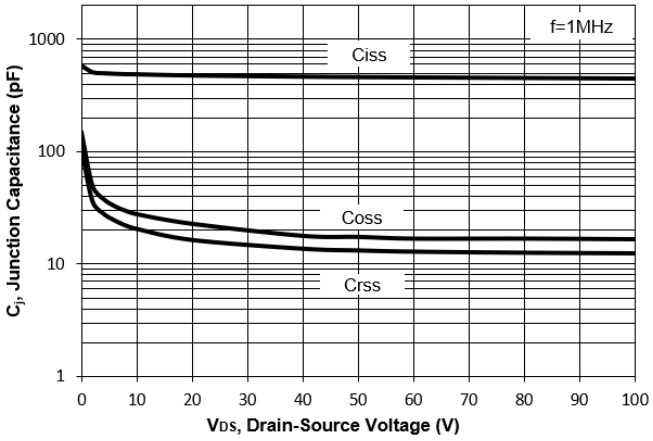


Fig. 8 Drain-Source Leakage Current vs. T_j

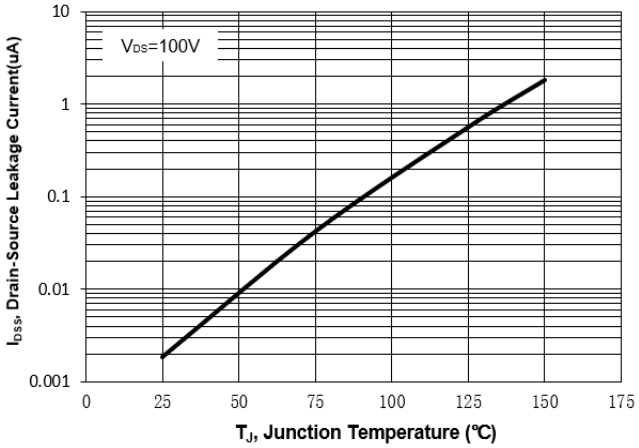


Fig. 9 $V_{(BR)DSS}$ vs. Junction Temperature

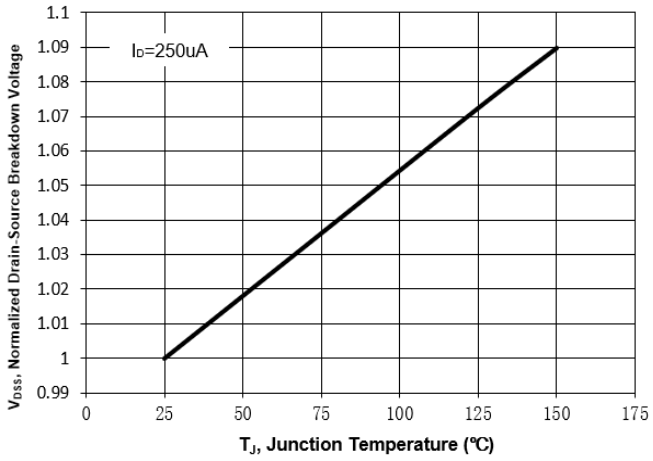


Fig. 10 Gate Threshold Variation vs. T_j

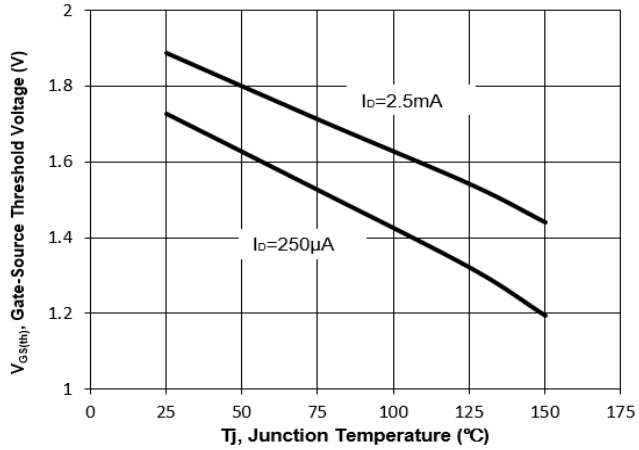
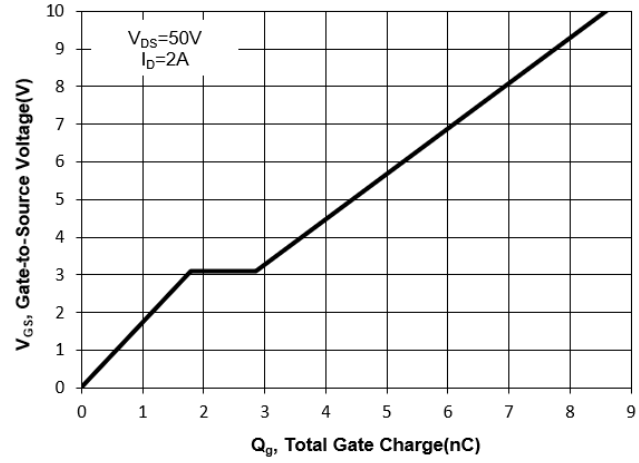


Fig. 11 Gate Charge



Test Circuits

Fig.1-1 Switching times test circuit

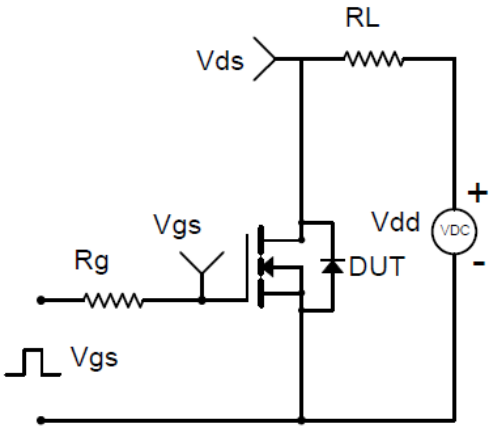


Fig.1-2 Switching Waveform

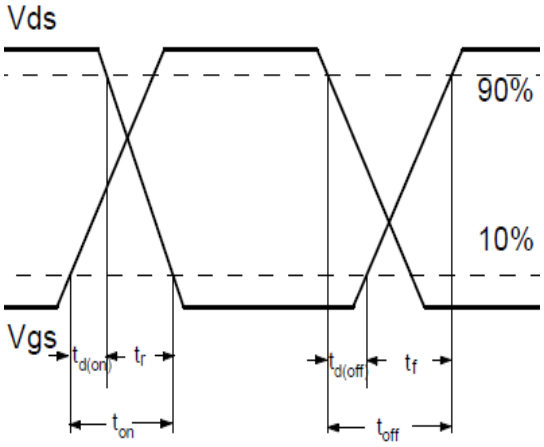


Fig.2-1 Gate charge test circuit

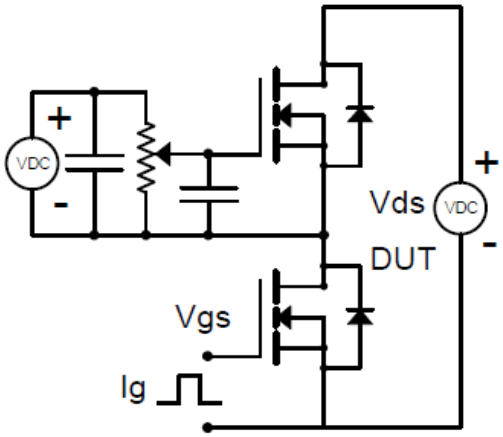
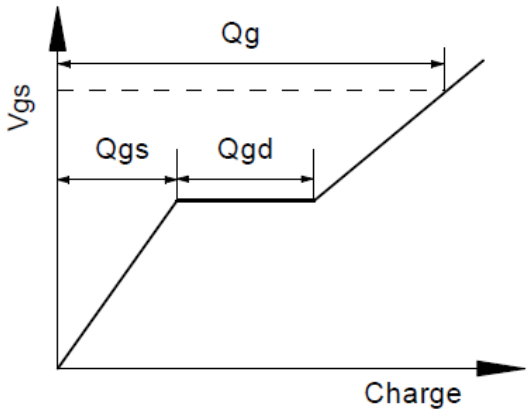
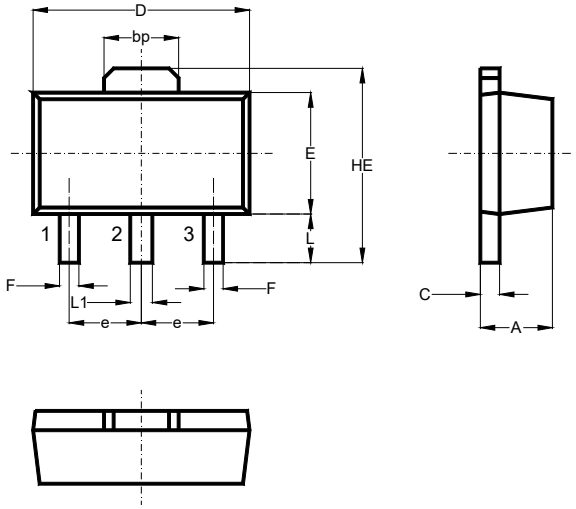


Fig.2-2 Gate charge waveform

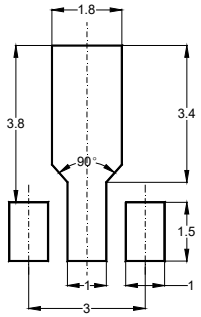


Package Outline (Dimensions in mm) SOT-89



Unit	A	bp	C	D	E	F	HE	e	L	L1
mm	1.6 1.4	1.60 1.50	0.5 0.3	4.6 4.4	2.6 2.4	0.45 0.35	4.25 3.75	1.5 typ.	1.05 0.95	0.51 0.41

Recommended Soldering Footprint



Packing information

Package	Tape Width (mm)	Pitch		Reel Size		Per Reel Packing Quantity
		mm	inch	mm	inch	
SOT-89	12	8 ± 0.1	0.315 ± 0.004	178	7	1,000
				330	13	4,000

Marking information

" MU10N280L " = Part No.
" YM " = Date Code Marking
" Y " = Year
" M " = Month
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