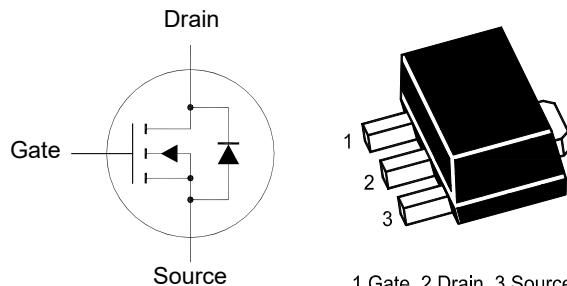


# MU10N125L

## N-Channel Enhancement Mode MOSFET

### Features

- Low Leakage Current



### Applications

- Portable appliances
- Battery management

### Absolute Maximum Ratings (at $T_a = 25^\circ\text{C}$ unless otherwise specified)

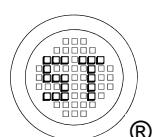
Parameter	Symbol	Value	Unit
Drain-Source Voltage	$V_{DS}$	100	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Drain Current $T_C = 25^\circ\text{C}$	$I_D$	10	A
Peak Drain Current, Pulsed <sup>1)</sup>	$I_{DM}$	30	A
Power Dissipation $T_C = 25^\circ\text{C}$	$P_D$	20	W
Operating Junction Temperature	$T_J$	150	$^\circ\text{C}$
Storage Temperature Range	$T_{stg}$	- 55 to + 150	$^\circ\text{C}$

### Thermal Resistance Ratings

Parameter	Symbol	Max.	Unit
Thermal Resistance from Junction to Case	$R_{\theta JC}$	6.25	$^\circ\text{C/W}$
Thermal Resistance from Junction to Ambient <sup>2)</sup>	$R_{\theta JA}$	100	$^\circ\text{C/W}$

<sup>1)</sup> Pulse Test: Pulse Width  $\leq 100 \mu\text{s}$ , Duty Cycle  $\leq 2\%$ , Repetitive rating, pulse width limited by junction temperature  $T_{J(MAX)}=150^\circ\text{C}$ .

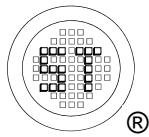
<sup>2)</sup> Device mounted on FR-4 substrate PC board, 2oz copper, with 1-inch square copper plate in still air.



# MU10N125L

Characteristics at  $T_a = 25^\circ\text{C}$  unless otherwise specified

Parameter	Symbol	Min.	Typ.	Max.	Unit
<b>STATIC PARAMETERS</b>					
Drain-Source Breakdown Voltage at $I_D = 250 \mu\text{A}$	$V_{(\text{BR})\text{DSS}}$	100	-	-	V
Zero Gate Voltage Drain Current at $V_{DS} = 80 \text{ V}$	$I_{\text{DSS}}$	-	-	1	$\mu\text{A}$
Gate-Source Leakage at $V_{GS} = \pm 20 \text{ V}$	$I_{\text{GSS}}$	-	-	$\pm 100$	nA
Gate-Source Threshold Voltage at $V_{DS} = V_{GS}, I_D = 250 \mu\text{A}$	$V_{GS(\text{th})}$	1	-	2.5	V
Drain-Source On-State Resistance at $V_{GS} = 10 \text{ V}, I_D = 5 \text{ A}$ at $V_{GS} = 4.5 \text{ V}, I_D = 4 \text{ A}$	$R_{\text{DS(on)}}$	-	-	120 125	$\text{m}\Omega$
<b>DYNAMIC PARAMETERS</b>					
Gate resistance at $V_{DS} = 0 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$	$R_g$	-	1.54	-	$\Omega$
Forward Transconductance at $V_{DS} = 10 \text{ V}, I_D = 5 \text{ A}$	$g_{\text{Fs}}$	-	6	-	S
Input Capacitance at $V_{DS} = 50 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$	$C_{\text{iss}}$	-	1306	-	pF
Output Capacitance at $V_{DS} = 50 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$	$C_{\text{oss}}$	-	35	-	pF
Reverse Transfer Capacitance at $V_{DS} = 50 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$	$C_{\text{rss}}$	-	28	-	pF
Total Gate Charge at $V_{DS} = 50 \text{ V}, I_D = 5 \text{ A}, V_{GS} = 10 \text{ V}$ at $V_{DS} = 50 \text{ V}, I_D = 5 \text{ A}, V_{GS} = 4.5 \text{ V}$	$Q_g$	- -	22 10	-	nC
Gate Source Charge at $V_{DS} = 50 \text{ V}, I_D = 5 \text{ A}, V_{GS} = 10 \text{ V}$	$Q_{gs}$	-	5	-	nC
Gate Drain Charge at $V_{DS} = 50 \text{ V}, I_D = 5 \text{ A}, V_{GS} = 10 \text{ V}$	$Q_{gd}$	-	2.7	-	nC
Turn-On Delay Time at $V_{DS} = 50 \text{ V}, I_D = 5 \text{ A}, V_{GS} = 10 \text{ V}, R_G = 3 \Omega$	$t_{d(\text{on})}$	-	15	-	ns
Turn-On Rise Time at $V_{DS} = 50 \text{ V}, I_D = 5 \text{ A}, V_{GS} = 10 \text{ V}, R_G = 3 \Omega$	$t_r$	-	6	-	ns
Turn-Off Delay Time at $V_{DS} = 50 \text{ V}, I_D = 5 \text{ A}, V_{GS} = 10 \text{ V}, R_G = 3 \Omega$	$t_{d(\text{off})}$	-	13.5	-	ns
Turn-Off Fall Time at $V_{DS} = 50 \text{ V}, I_D = 5 \text{ A}, V_{GS} = 10 \text{ V}, R_G = 3 \Omega$	$t_f$	-	2.2	-	ns
<b>Body-Diode PARAMETERS</b>					
Drain-Source Diode Forward Voltage at $V_{GS} = 0 \text{ V}, I_s = 1 \text{ A}$	$V_{SD}$	-	-	1.2	V
Body Diode Reverse Recovery Time at $I_s = 5 \text{ A}, di/dt = 100 \text{ A}/\mu\text{s}$	$t_{rr}$	-	21	-	ns
Body Diode Reverse Recovery Charge at $I_s = 5 \text{ A}, di/dt = 100 \text{ A}/\mu\text{s}$	$Q_{rr}$	-	22	-	nC



# MU10N125L

## Electrical Characteristics Curves

Fig. 1 Typical Output Characteristic

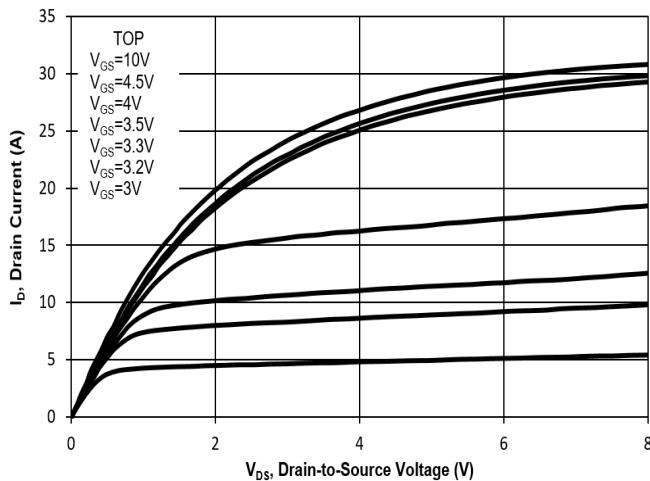


Fig. 2 Typical Transfer Characteristic

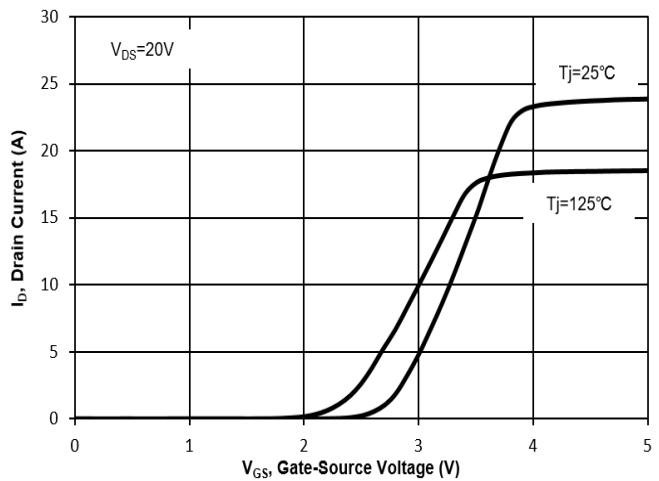


Fig. 3 On-Resistance vs. Drain Current

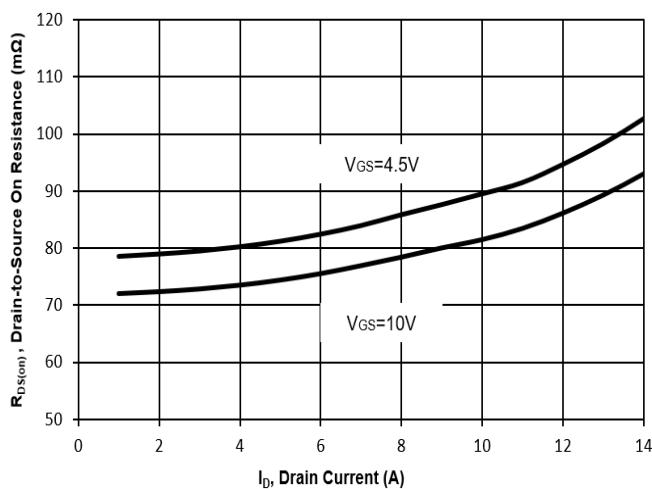


Fig. 4 On-Resistance vs. Gate Voltage

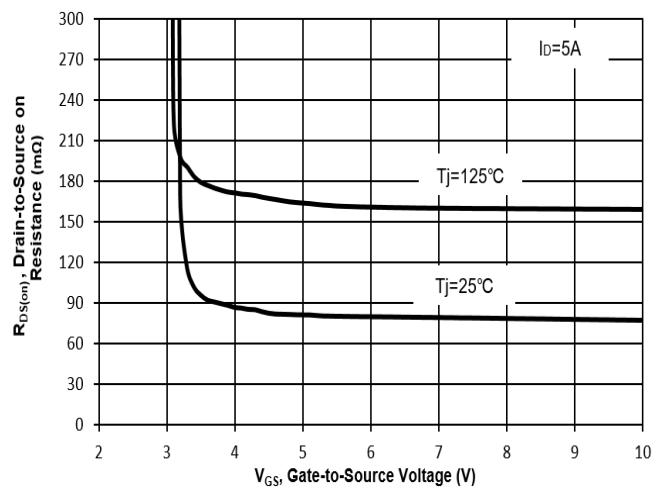


Fig. 5 On-Resistance vs.  $T_j$

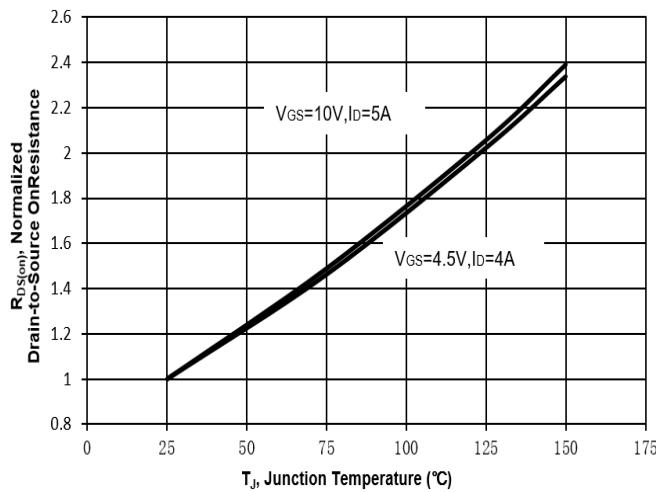
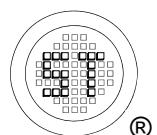
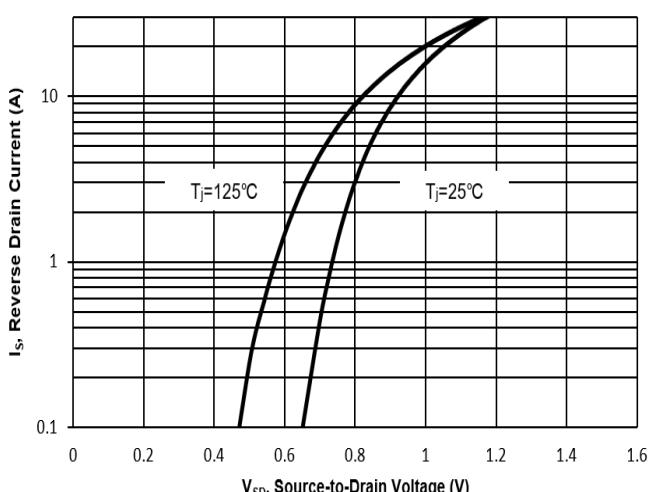


Fig. 6 Typical Body-Diode Forward Characteristic



# MU10N125L

## Electrical Characteristics Curves

Fig. 7 Typical Junction Capacitance

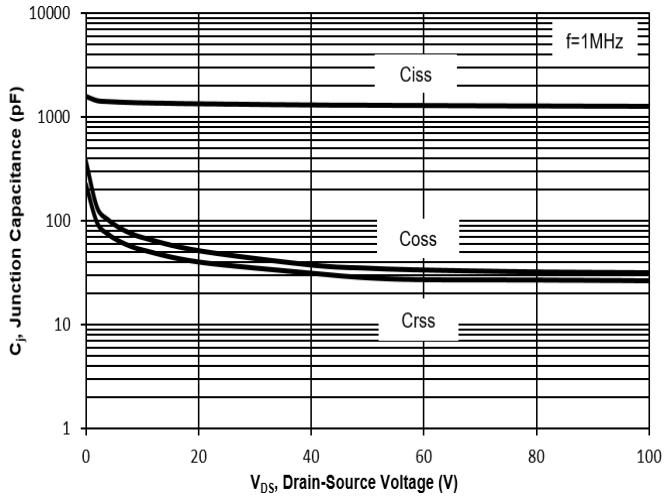


Fig. 8 Drain-Source Leakage Current vs.  $T_J$

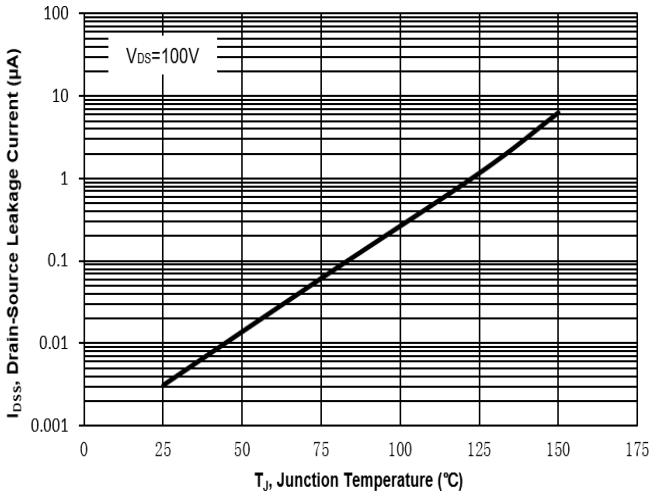


Fig. 9  $V_{(BR)DSS}$  vs. Junction Temperature

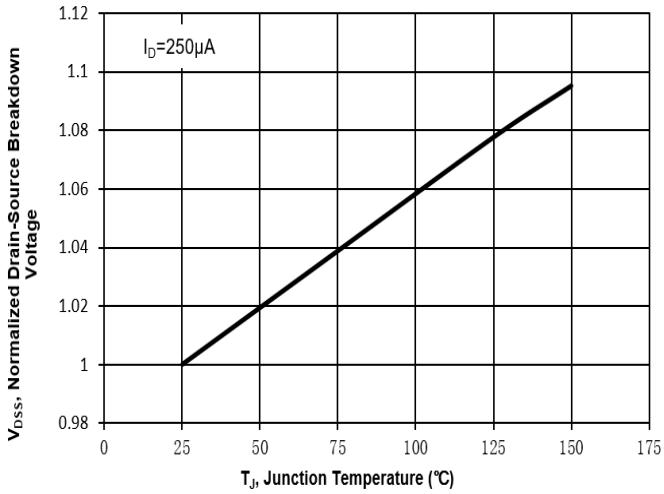


Fig. 10 Gate Threshold Variation vs.  $T_J$

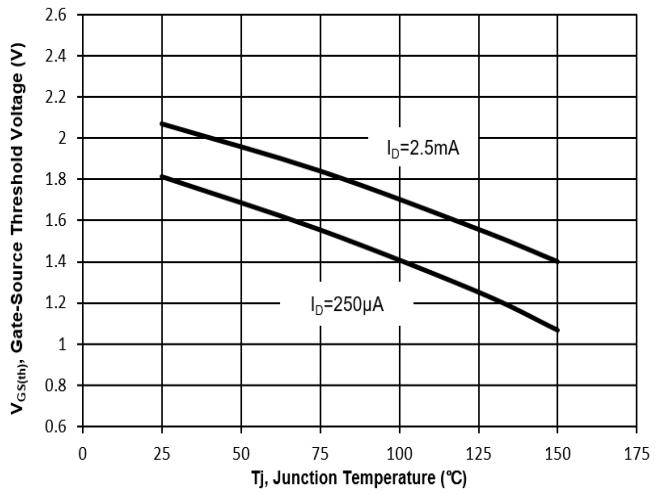
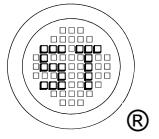
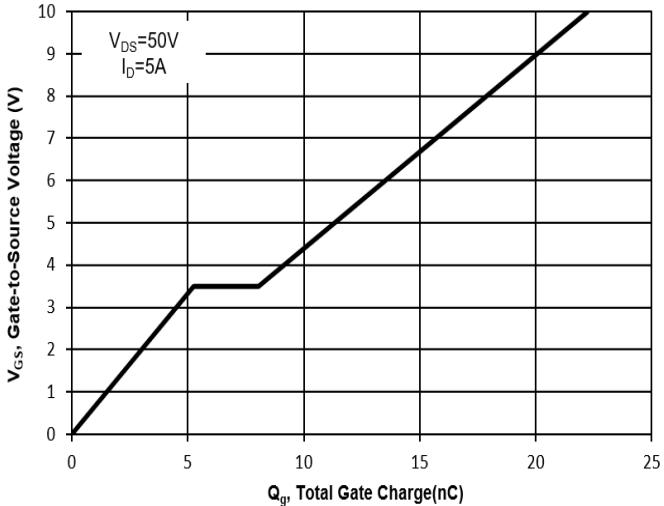


Fig. 11 Gate Charge



# MU10N125L

## Test Circuits

Fig.1-1 Switching times test circuit

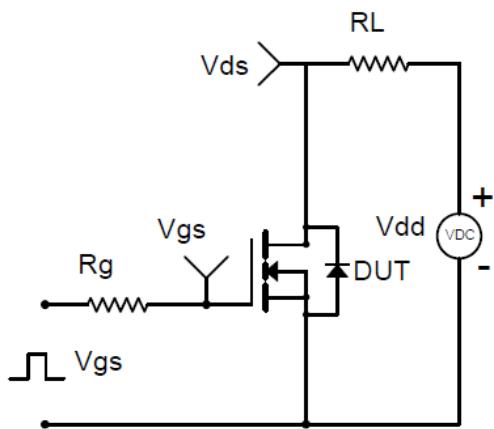


Fig.1-2 Switching Waveform

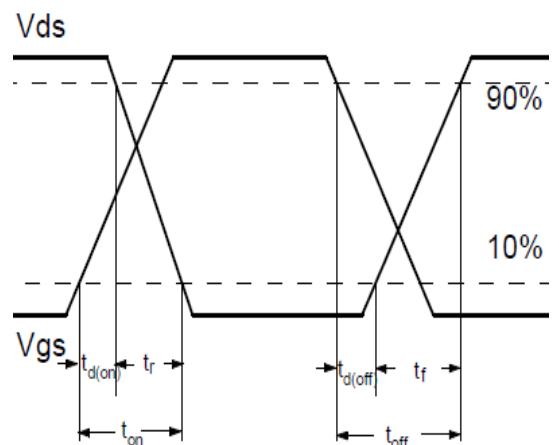


Fig.2-1 Gate charge test circuit

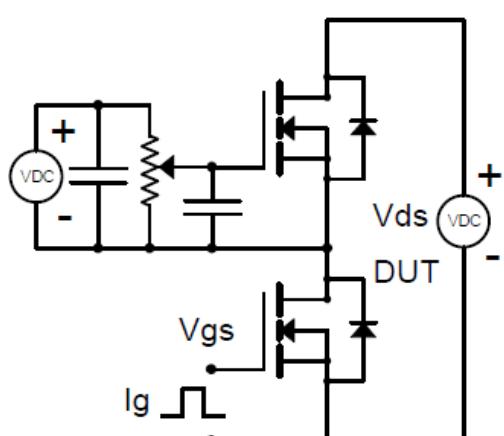
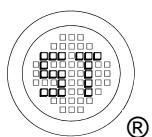
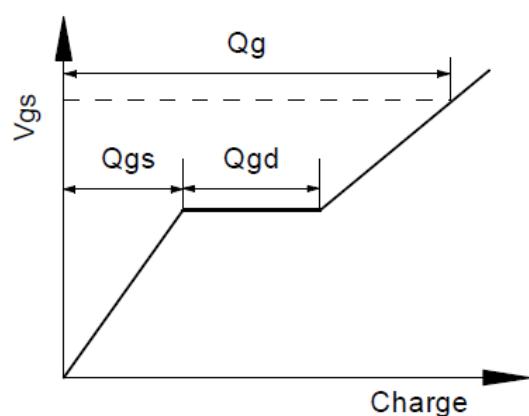


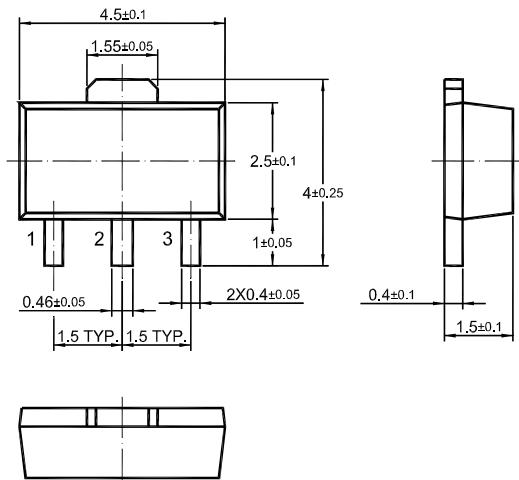
Fig.2-2 Gate charge waveform



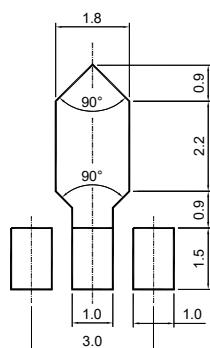
# MU10N125L

## Package Outline (Dimensions in mm)

SOT-89



## Recommended Soldering Footprint



## Packing information

Package	Tape Width (mm)	Pitch		Reel Size		Per Reel Packing Quantity
		mm	inch	mm	inch	
SOT-89	12	8 ± 0.1	0.315 ± 0.004	178	7	1,000
				330	13	4,000

## Marking information

" MU10N125L " = Part No.

"YM" = Date Code Marking

"Y" = Year

"M" = Month

Font type: Arial

