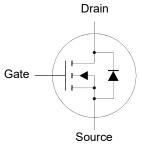
N-Channel Enhancement Mode MOSFET

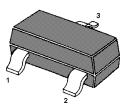
Features

Surface-mounted package

Applications

- Portable appliances
- Battery management





1. Gate 2. Source 3. Drain SOT-23 Plastic Package

Absolute Maximum Ratings (at T_a = 25°C unless otherwise specified)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V _{DS}	20	V
Drain-Gate Voltage	V _{GS}	± 12	V
Drain Current	ID	6	А
Peak Drain Current, Pulsed ¹⁾	Ідм	20	А
Total Power Dissipation ²⁾	P _{tot}	740	mW
Operating Junction and Storage Temperature Range	Tj, Tstg	- 55 to + 150	°C

Thermal Characteristics

Parameter	Symbol	Max.	Unit	
Thermal Resistance from Junction to Ambient ²⁾	$R_{\theta JA}$	167	°C/W	

¹⁾ Pulse Test: Pulse Width \leq 100 µs, Duty Cycle \leq 2%, Repetitive rating, pulse width limited by junction temperature T_{J(MAX)} = 150°C.

²⁾ Device mounted on FR-4 substrate PC board, 2oz copper, with 1-inch square copper plate in still air.

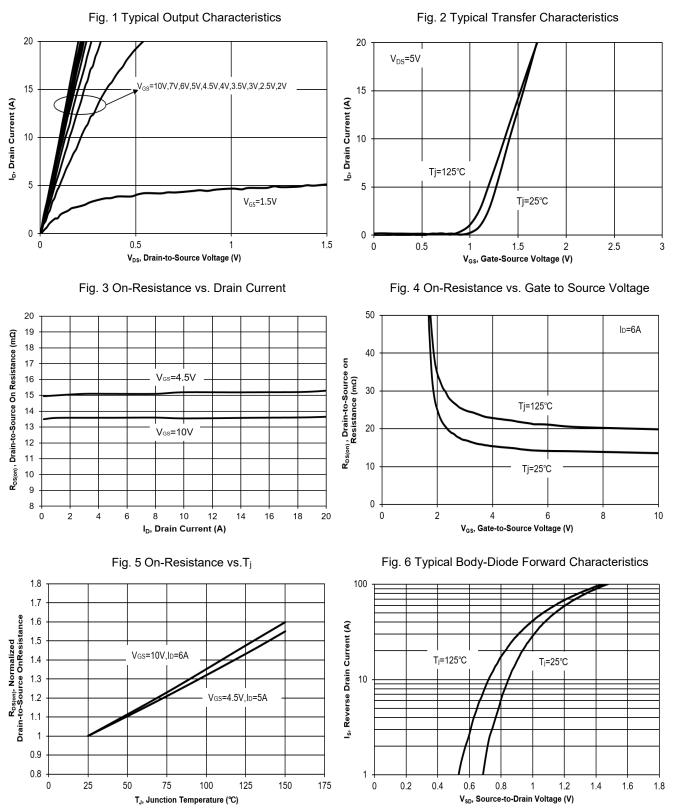


Characteristics at $T_a = 25^{\circ}C$ unless otherwise specified

Parameter	Symbol	Min.	Тур.	Max.	Unit
STATIC PARAMETERS					
Drain-Source Breakdown Voltage at I₀ = 250 μA	BV _{DSS}	20	-	-	V
Drain-Source Leakage Current at V _{DS} = 20 V	IDSS	-	-	1	μA
Gate-Source Leakage Current at V _{GS} = ± 12 V	lgss	-	-	± 100	nA
Gate-Source Threshold Voltage at V_{DS} = V_{GS} , I_D = 250 μ A	V _{GS(th)}	0.4	-	1.2	V
Drain-Source On-State Resistance at $V_{GS} = 10 \text{ V}$, $I_D = 6 \text{ A}$ at $V_{GS} = 4.5 \text{ V}$, $I_D = 5 \text{ A}$ at $V_{GS} = 2.5 \text{ V}$, $I_D = 4 \text{ A}$ at $V_{GS} = 1.8 \text{ V}$, $I_D = 2 \text{ A}$	R _{DS(on)}	- - -	- - -	29 35 48 91	mΩ
DYNAMIC PARAMETERS					
Forward Transconductance at V_{DS} = 5 V, I_D = 4 A	g _{fs}	-	12.6	-	S
Gate Resistance at V _{GS} = 0 V, V _{DS} = 0 V, f = 1 MHz	R _g	-	2.9	-	Ω
Input Capacitance at V _{GS} = 0 V, V _{DS} = 10 V, f = 1 MHz	C _{iss}	-	871	-	pF
Output Capacitance at V _{GS} = 0 V, V _{DS} = 10 V, f = 1 MHz	Coss	-	117	-	pF
Reverse Transfer Capacitance at V_{GS} = 0 V, V_{DS} = 10 V, f = 1 MHz	Crss	-	87	-	pF
Gate charge total at V_{DS} = 10 V, V_{GS} = 10 V, I_D = 6 A at V_{DS} = 10 V, V_{GS} = 4.5 V, I_D = 6 A	Qg	-	21.2 9.6	-	nC
Gate to Source Charge at V_{DS} = 10 V, V_{GS} = 10 V, I_D = 6 A	Q _{gs}	-	2.2	-	nC
Gate to Drain Charge at V_{DS} = 10 V, V_{GS} = 10 V, I_D = 6 A	Q_{gd}	-	2.3	-	nC
Turn-On Delay Time at V _{DS} = 10 V, V _{GS} = 4.5 V, I _D = 6 A, R _g = 3.3 Ω	t _{d(on)}	-	14	-	ns
Turn-On Rise Time at V _{DS} = 10 V, V _{GS} = 4.5 V, I _D = 6 A, R _g = 3.3 Ω	tr	-	56	-	ns
Turn-Off Delay Time at V _{DS} = 10 V, V _{GS} = 4.5 V, I _D = 6 A, R _g = 3.3 Ω	$t_{d(off)}$	-	18	-	ns
Turn-Off Fall Time at V _{DS} = 10 V, V _{GS} = 4.5 V, I _D = 6 A, R _g = 3.3 Ω	t _f	-	8.5	-	ns
Body-Diode PARAMETERS					
Drain-Source Diode Forward Voltage at Is = 1 A	V _{SD}	-	-	1	V
Body-Diode Continuous Current	ls	-	-	6	А
Body Diode Reverse Recovery Time at I _s = 6 A, di/dt = 100 A / μs	trr	-	8	-	ns
Body Diode Reverse Recovery Charge at Is = 6 A, di/dt = 100 A / μs	Qrr	-	2.4	-	nC



Electrical Characteristics Curves





Electrical Characteristics Curves

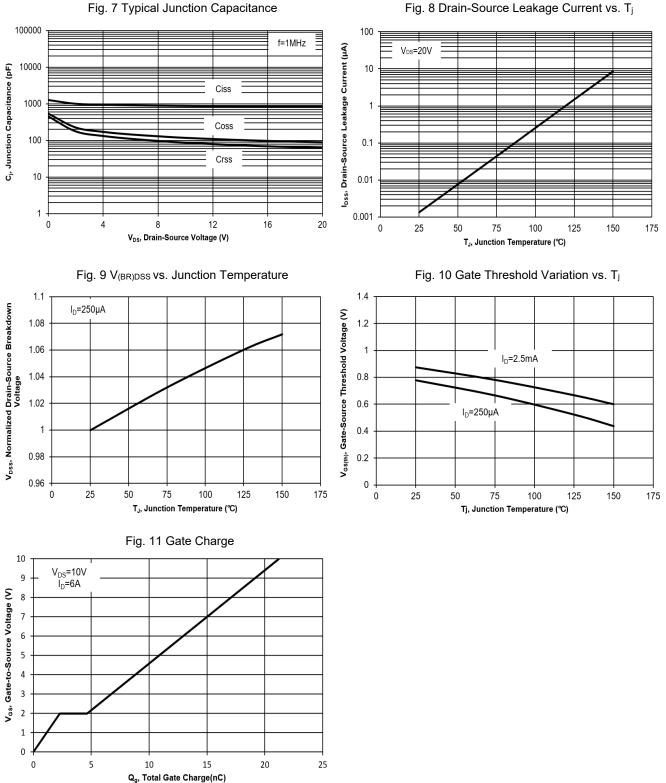
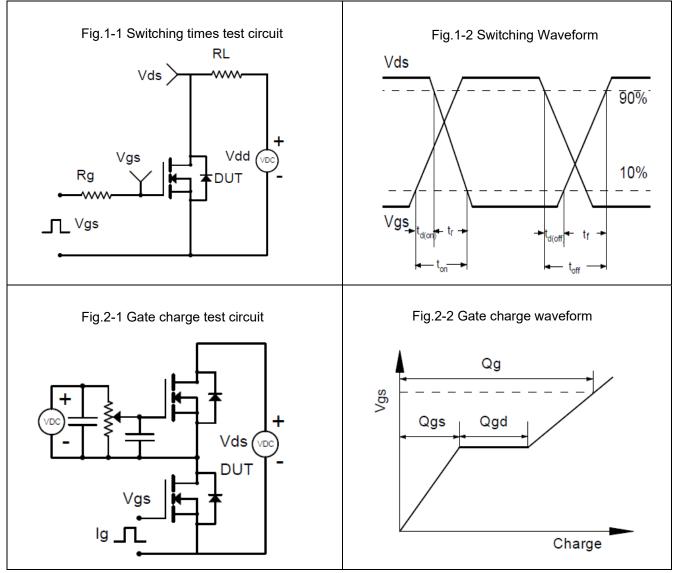




Fig. 8 Drain-Source Leakage Current vs. T_j

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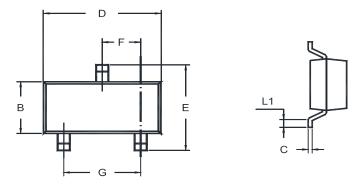
Test Circuits

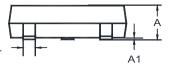




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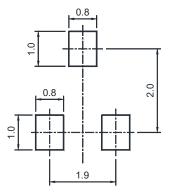
Package Outline (Dimensions in mm)





Unit	А	A1	В	С	D	E	F	G	L	L1
	1.20	0.100	1.40	0.19	3.04	2.6	1.02	2.04	0.51	0.2
mm	0.89	0.013	1.20	0.08	2.80	2.2	0.89	1.78	0.37	MIN

Recommended Soldering Footprint



Packing information

	Tape Width	Pitch		Reel	Size	
Package	(mm)	mm	inch	mm	inch	Per Reel Packing Quantity
SOT-23	8	4 ± 0.1	0.157 ± 0.004	178	7	3,000

Marking information

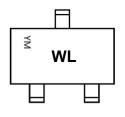
" WL " = Part No.

" YM " = Date Code Marking

" Y " = Year

" M " = Month

Font type: Arial



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SOT-23