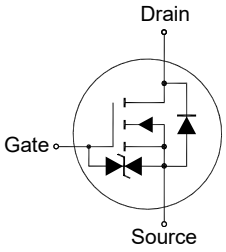
MMFTN3018K-AH

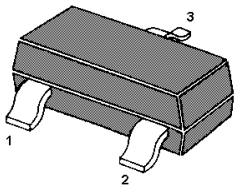
N-Channel Enhancement Mode MOSFET

Features

- AEC-Q101 Qualified
- Surface-mounted package
- Built-in G-S Protection Diode
- Halogen and Antimony Free(HAF), RoHS compliant
- Typical ESD Protection HBM Class 1C

Classification	Voltage Range(V)
0A	< 125
0B	125 to < 250
1A	250 to < 500
1B	500 to < 1000
1C	1000 to < 2000
2	2000 to < 4000
3A	4000 to < 8000
3B	≥ 8000





1. Gate 2. Source 3. Drain
SOT-23 Plastic Package

Application

- Portable appliances
- Battery management

Absolute Maximum Ratings (at T_a = 25℃ unless otherwise specified)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DS}	30	V
Gate-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current	I_D	100	mA
Peak Drain Current, Pulsed [1]	I_{DM}	400	mA
Total Power Dissipation [2]	P_{tot}	350	mW
Operating Junction and Storage Temperature Range	T_J, T_{stg}	- 55 to + 150	℃

Thermal Characteristics

Parameter	Symbol	Max.	Unit
Thermal Resistance from Junction to Ambient [2]	$R_{\theta JA}$	357	℃/W

[1] Pw ≤ 10 μs, duty cycle ≤ 1%.

[2] Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.

Dated: 25/12/2023 Rev:01

Characteristics at T_a = 25℃ unless otherwise specified

Parameter	Symbol	Min.	Typ.	Max.	Unit
STATIC PARAMETERS					
Drain-Source Breakdown Voltage at I_D = 10 µA	$V_{(BR)DSS}$	30	-	-	V
Zero Gate Voltage Drain Current at V_{DS} = 24 V	I_{DSS}	-	-	1	µA
Gate-source Leakage at V_{GS} = ± 20 V	I_{GSS}	-	-	± 1	µA
Gate Source Threshold Voltage at V_{DS} = 3 V, I_D = 100 µA	$V_{GS(th)}$	0.8	-	1.5	V
Static Drain Source On-State Resistance at V_{GS} = 4 V, I_D = 10 mA at V_{GS} = 2.5 V, I_D = 1 mA	$R_{DS(on)}$	- -	- -	8 13	Ω
DYNAMIC PARAMETERS					
Forward Transconductance at V_{DS} = 10 V, I_D = 0.2 A	g_{fs}	-	0.71	-	S
Input Capacitance at V_{GS} = 0 V, V_{DS} = 25 V, f = 1 MHz	C_{iss}	-	32	-	pF
Output Capacitance at V_{GS} = 0 V, V_{DS} = 25 V, f = 1 MHz	C_{oss}	-	10.2	-	pF
Reverse Transfer Capacitance at V_{GS} = 0 V, V_{DS} = 25 V, f = 1 MHz	C_{rss}	-	7.5	-	pF
Turn-On Delay Time at V_{GS} = 10 V, V_{DS} = 30 V, R_G = 25 Ω, I_D = 0.4 A	$t_{d(on)}$	-	5.4	-	ns
Turn-On Rise Time at V_{GS} = 10 V, V_{DS} = 30 V, R_G = 25 Ω, I_D = 0.4 A	t_r	-	2.7	-	ns
Turn-Off Delay Time at V_{GS} = 10 V, V_{DS} = 30 V, R_G = 25 Ω, I_D = 0.4 A	$t_{d(off)}$	-	5.8	-	ns
Turn-Off Fall Time at V_{GS} = 10 V, V_{DS} = 30 V, R_G = 25 Ω, I_D = 0.4 A	t_f	-	30	-	ns
Body-Diode PARAMETERS					
Drain-Source Diode Forward Voltage at I_S = 115 mA	V_{SD}	-	-	1.2	V
Body-Diode Continuous Current	I_S	-	-	100	mA

Electrical Characteristics Curves

Fig. 1 Typical Output Characteristics

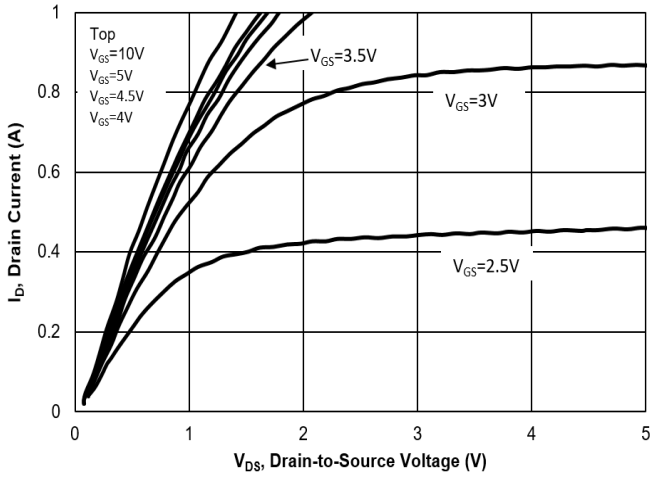


Fig. 2 Typical Transfer Characteristics

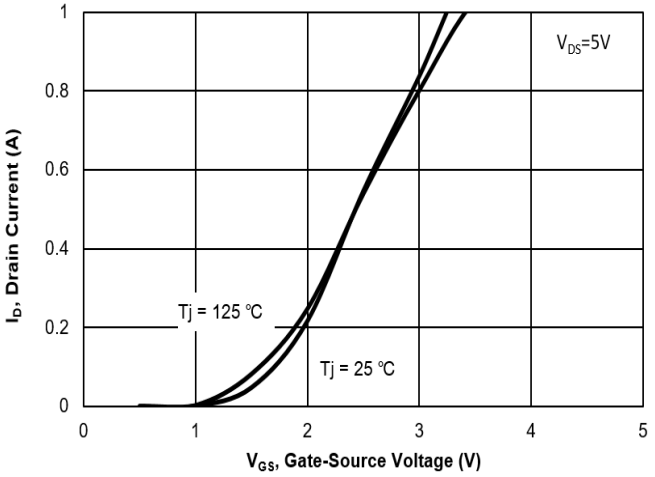


Fig. 3 On-Resistance vs. Drain Current

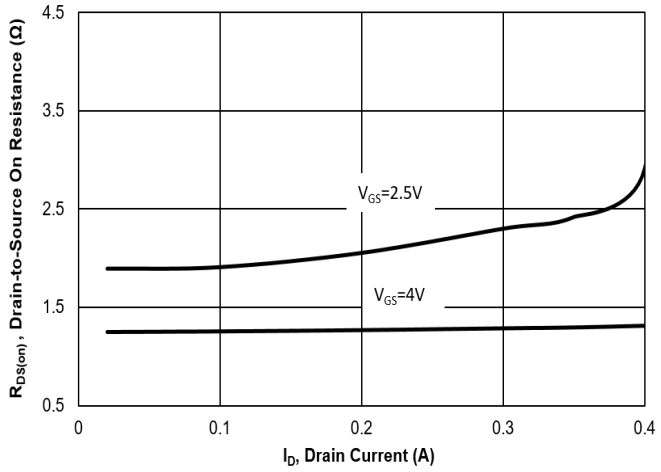


Fig. 4 On-Resistance vs. Gate-Source Voltage

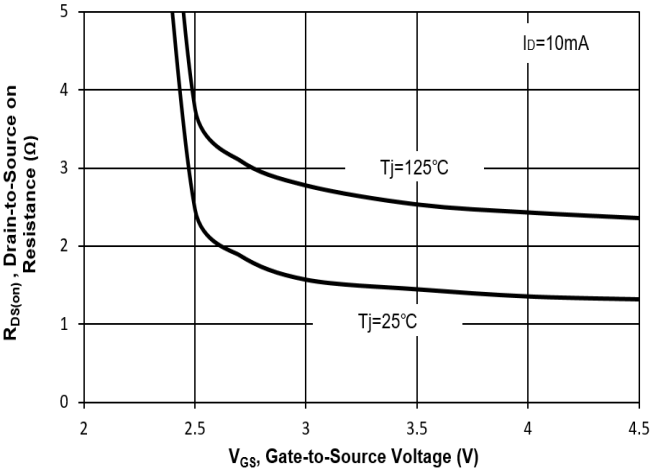


Fig. 5 On-Resistance vs.T_j

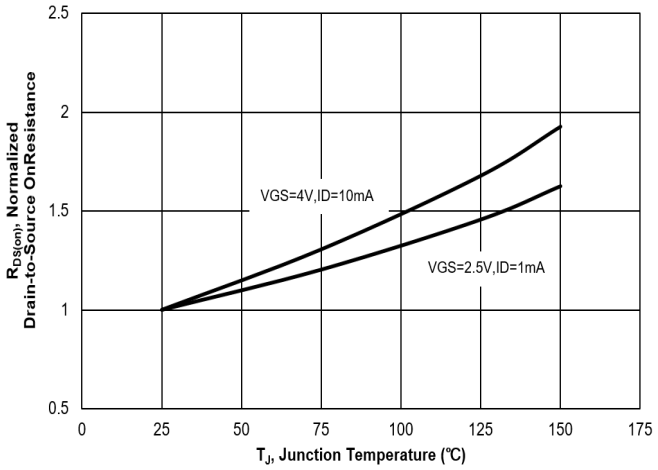
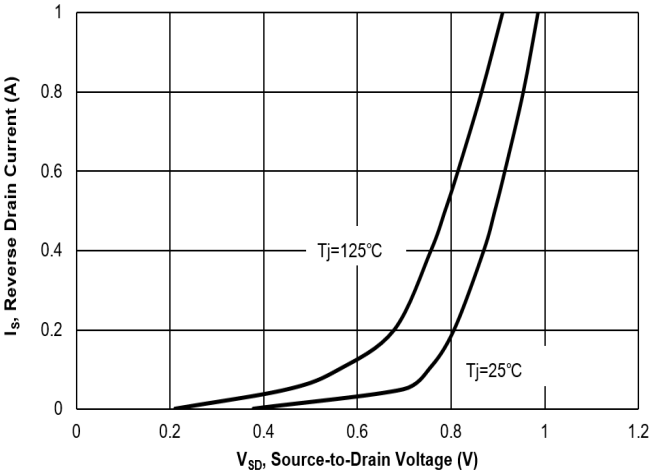


Fig. 6 Typical Body-Diode Forward Characteristics



Electrical Characteristics Curves

Fig. 7 Typical Junction Capacitance

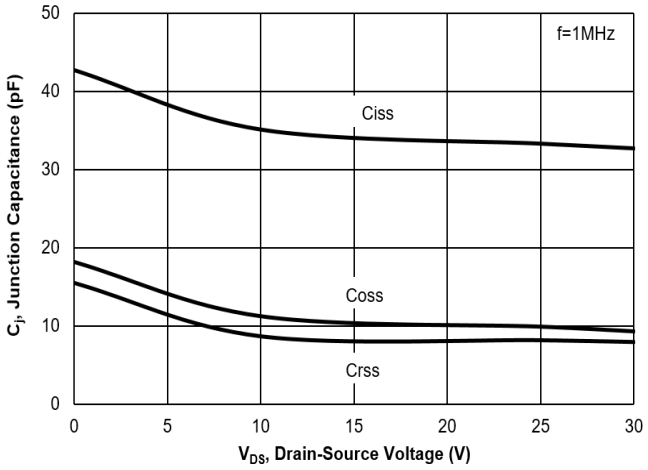


Fig. 8 Gate Threshold Variation vs. T_j

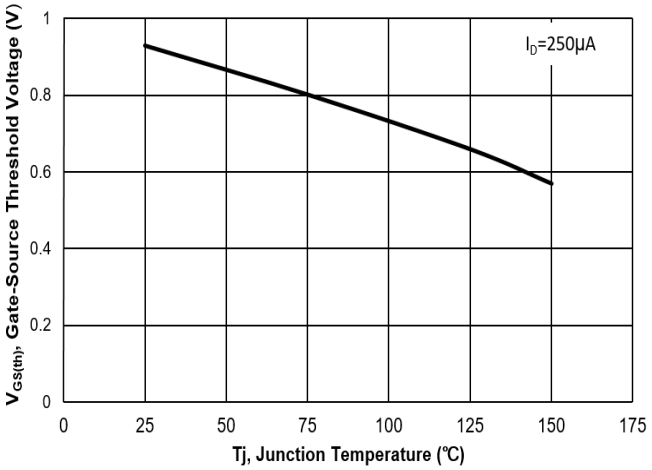
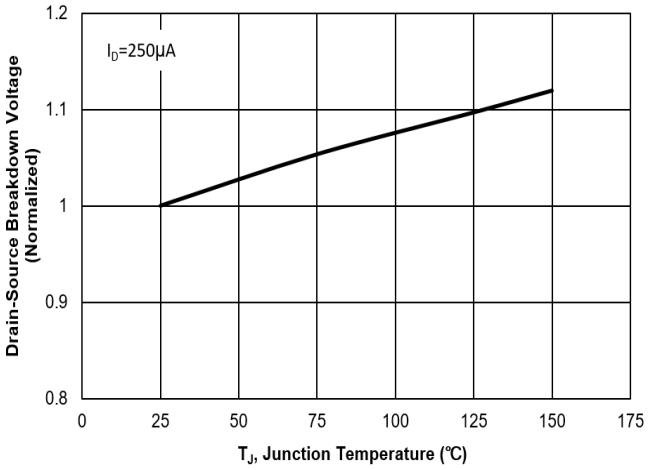


Fig. 9 $V_{(BR)DSS}$ vs. Junction Temperature



Test Circuits

Fig.1-1 Switching times test circuit

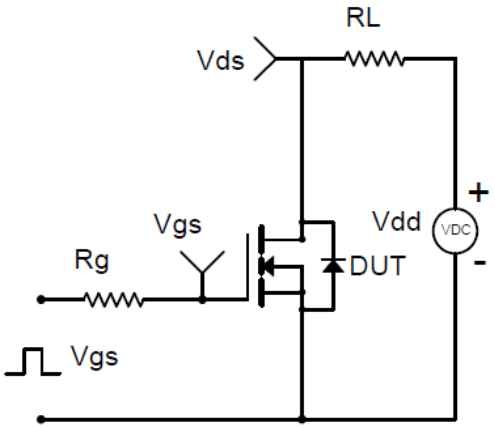


Fig.1-2 Switching Waveform

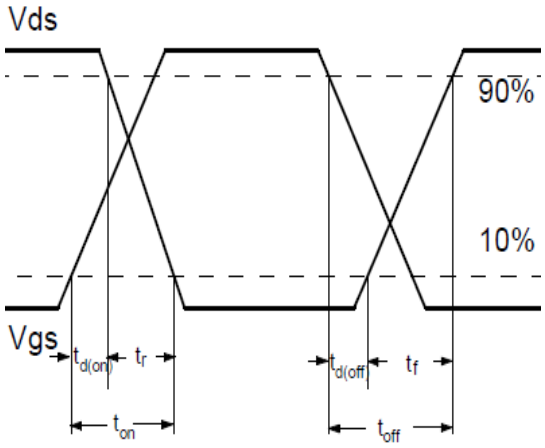


Fig.2-1 Gate charge test circuit

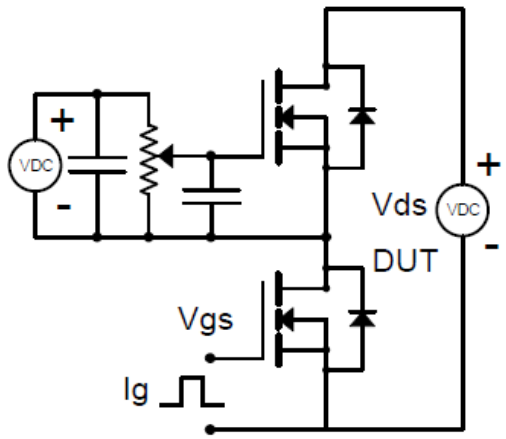
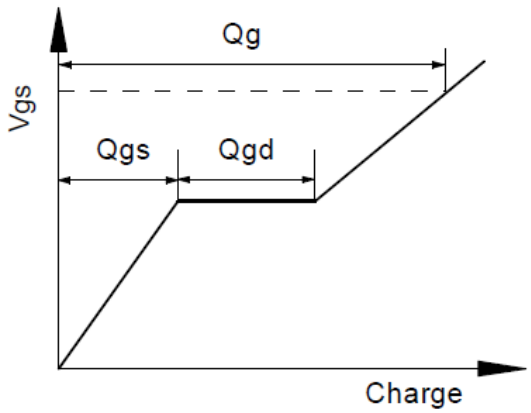
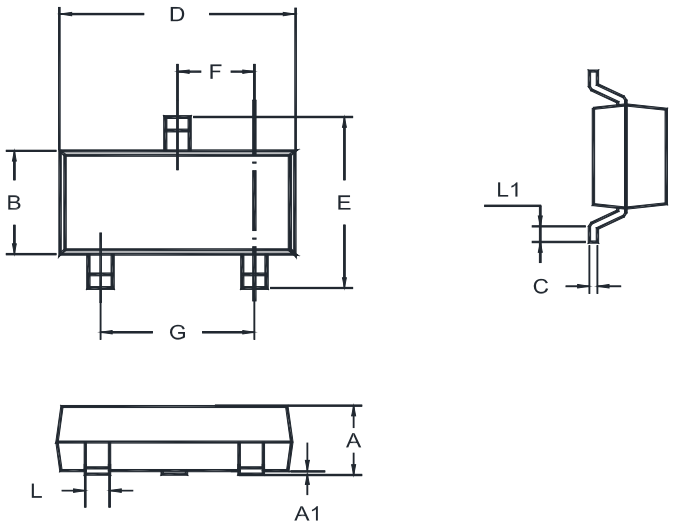


Fig.2-2 Gate charge waveform

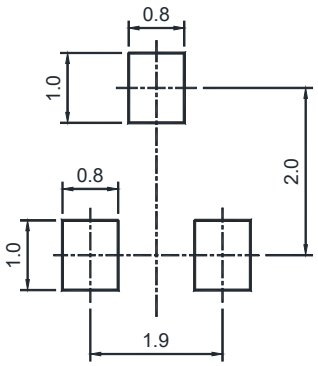


Package Outline (Dimensions in mm) SOT-23



Unit	A	A1	B	C	D	E	F	G	L	L1
mm	1.20 0.89	0.100 0.013	1.40 1.20	0.19 0.08	3.04 2.80	2.6 2.2	1.02 0.89	2.04 1.78	0.51 0.37	0.2 MIN

Recommended Soldering Footprint

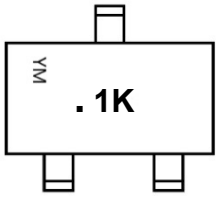


Packing information

Package	Tape Width (mm)	Pitch		Reel Size		Per Reel Packing Quantity
		mm	inch	mm	inch	
SOT-23	8	4 ± 0.1	0.157 ± 0.004	178	7	3,000

Marking information

" 1K " = Part No.
" • " = HAF (Halogen and Antimony Free)
" YM " = Date Code Marking
" Y " = Year
" M " = Month
Font type: Arial



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