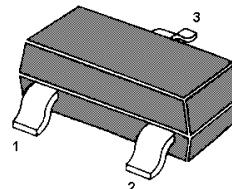
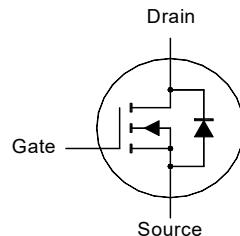


# MMFTN2362

## N-Channel Enhancement Mode MOSFET

### Features

- Surface-mounted package



1. Gate 2. Source 3. Drain  
SOT-23 Plastic Package

### Applications

- Portable appliances
- Battery management

### Absolute Maximum Ratings (at $T_a = 25^\circ\text{C}$ unless otherwise specified)

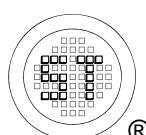
Parameter	Symbol	Value	Unit
Drain-Source Voltage	$V_{DS}$	60	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Continuous Drain Current	$I_D$	3	A
Drain Current - Pulsed <sup>1)</sup>	$I_{DM}$	12	A
Power Dissipation <sup>2)</sup>	$P_{tot}$	1.25	W
Operating Junction and Storage Temperature Range	$T_j, T_{stg}$	- 55 to + 150	°C

### Thermal Resistance Ratings

Parameter	Symbol	Max.	Unit
Thermal Resistance from Junction to Ambient <sup>2)</sup>	$R_{\theta JA}$	100	°C/W

<sup>1)</sup> Pulse Test: Pulse Width  $\leq 100 \mu\text{s}$ , Duty Cycle  $\leq 2\%$ , Repetitive rating, pulse width limited by junction temperature  $T_{J(MAX)}=150^\circ\text{C}$ .

<sup>2)</sup> Device mounted on FR-4 substrate PC board, 2oz copper, with 1-inch square copper plate in still air,  $t \leq 10 \text{ s}$ .



# MMFTN2362

Characteristics at  $T_a = 25^\circ\text{C}$  unless otherwise specified

Parameter	Symbol	Min.	Typ.	Max.	Unit
<b>STATIC PARAMETERS</b>					
Drain-Source Breakdown Voltage at $I_D = 250 \mu\text{A}$	$V_{(\text{BR})\text{DSS}}$	60	-	-	V
Drain-Source Leakage Current at $V_{\text{DS}} = 48 \text{ V}$	$I_{\text{DSS}}$	-	-	1	$\mu\text{A}$
Gate-Source Leakage Current at $V_{\text{GS}} = \pm 16 \text{ V}$	$I_{\text{GSS}}$	-	-	$\pm 100$	nA
Gate-Source Threshold Voltage at $V_{\text{GS}} = V_{\text{DS}}, I_D = 250 \mu\text{A}$	$V_{\text{GS}(\text{th})}$	1	-	2.5	V
Drain-Source On-State Resistance at $V_{\text{GS}} = 10 \text{ V}, I_D = 3 \text{ A}$ at $V_{\text{GS}} = 4.5 \text{ V}, I_D = 2.4 \text{ A}$	$R_{\text{DS}(\text{on})}$	- -	- -	80 100	$\text{m}\Omega$
<b>DYNAMIC PARAMETERS</b>					
Forward Transconductance at $V_{\text{DS}} = 5 \text{ V}, I_D = 2 \text{ A}$	$g_{\text{FS}}$	-	4.2	-	S
Gate Resistance at $V_{\text{DS}} = 0 \text{ V}, V_{\text{GS}} = 0 \text{ V}, f = 1 \text{ MHz}$	$R_g$	-	1.3	-	$\Omega$
Input Capacitance at $V_{\text{DS}} = 30 \text{ V}, V_{\text{GS}} = 0 \text{ V}, f = 1 \text{ MHz}$	$C_{\text{iss}}$	-	445	-	pF
Output Capacitance at $V_{\text{DS}} = 30 \text{ V}, V_{\text{GS}} = 0 \text{ V}, f = 1 \text{ MHz}$	$C_{\text{oss}}$	-	22	-	pF
Reverse Transfer Capacitance at $V_{\text{DS}} = 30 \text{ V}, V_{\text{GS}} = 0 \text{ V}, f = 1 \text{ MHz}$	$C_{\text{rss}}$	-	18	-	pF
Gate Charge Total at $V_{\text{DS}} = 30 \text{ V}, V_{\text{GS}} = 10 \text{ V}, I_D = 2 \text{ A}$ at $V_{\text{DS}} = 30 \text{ V}, V_{\text{GS}} = 4.5 \text{ V}, I_D = 2 \text{ A}$	$Q_g$	- -	8.6 4	-	nC
Gate to Source Charge at $V_{\text{DS}} = 30 \text{ V}, V_{\text{GS}} = 10 \text{ V}, I_D = 2 \text{ A}$	$Q_{\text{gs}}$	-	1.8	-	nC
Gate to Drain Charge at $V_{\text{DS}} = 30 \text{ V}, V_{\text{GS}} = 10 \text{ V}, I_D = 2 \text{ A}$	$Q_{\text{gd}}$	-	1.2	-	nC
Turn-On Delay Time at $V_{\text{DS}} = 30 \text{ V}, V_{\text{GS}} = 10 \text{ V}, I_D = 2 \text{ A}, R_g = 4.7 \Omega$	$t_{\text{d}(\text{on})}$	-	7	-	ns
Turn-On Rise Time at $V_{\text{DS}} = 30 \text{ V}, V_{\text{GS}} = 10 \text{ V}, I_D = 2 \text{ A}, R_g = 4.7 \Omega$	$t_r$	-	2	-	ns
Turn-Off Delay Time at $V_{\text{DS}} = 30 \text{ V}, V_{\text{GS}} = 10 \text{ V}, I_D = 2 \text{ A}, R_g = 4.7 \Omega$	$t_{\text{d}(\text{off})}$	-	6	-	ns
Turn-Off Fall Time at $V_{\text{DS}} = 30 \text{ V}, V_{\text{GS}} = 10 \text{ V}, I_D = 2 \text{ A}, R_g = 4.7 \Omega$	$t_f$	-	5	-	ns
<b>BODY DIODE PARAMETERS</b>					
Drain-Source Diode Forward Voltage at $I_s = 1 \text{ A}$	$V_{\text{SD}}$	-	-	1.2	V
Body-Diode Continuous Current	$I_s$	-	-	3	A
Body Diode Reverse Recovery Time at $I_s = 2 \text{ A}, di/dt = 100 \text{ A} / \mu\text{s}$	$t_{\text{rr}}$	-	8	-	ns
Body Diode Reverse Recovery Charge at $I_s = 2 \text{ A}, di/dt = 100 \text{ A} / \mu\text{s}$	$Q_{\text{rr}}$	-	4	-	nC

# MMFTN2362

## Electrical Characteristics Curves

Fig. 1 Typical Output Characteristics

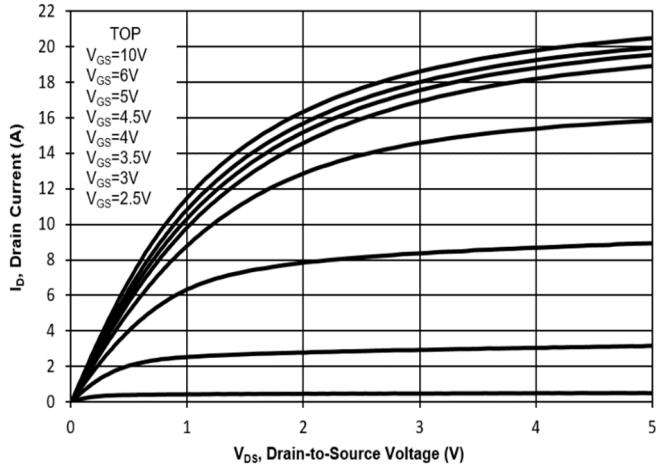


Fig. 2 Typical Transfer Characteristics

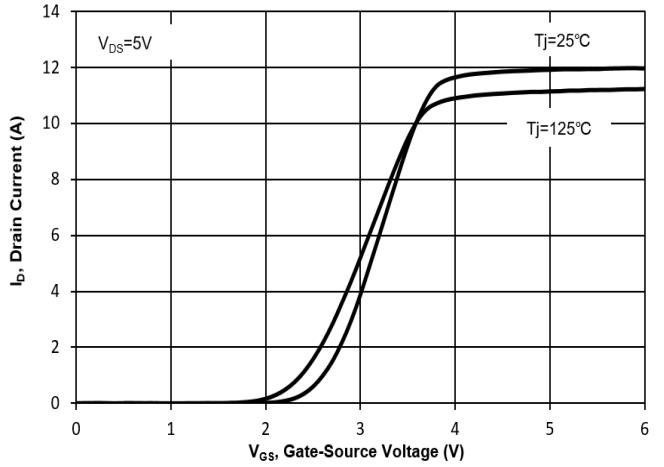


Fig. 3 On-Resistance vs. Drain Current

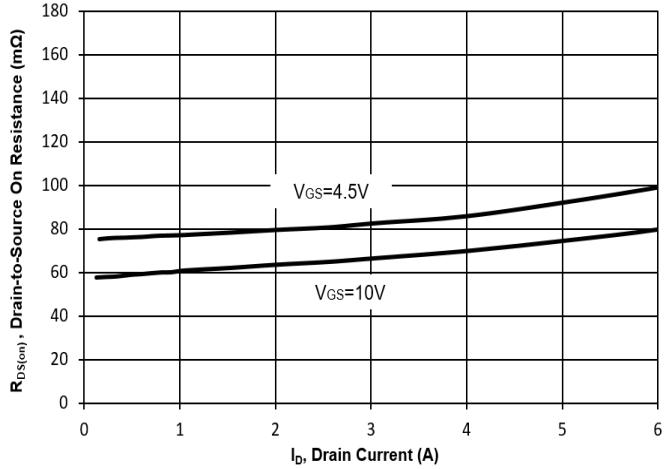


Fig. 4 On-Resistance vs. Gate-Source Voltage

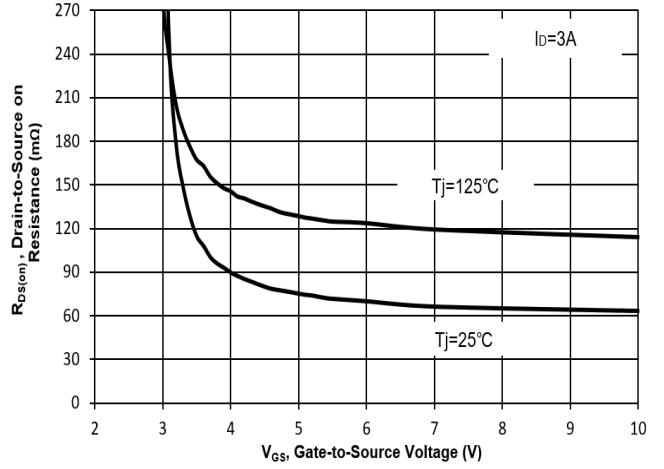


Fig. 5 On-Resistance vs.  $T_j$

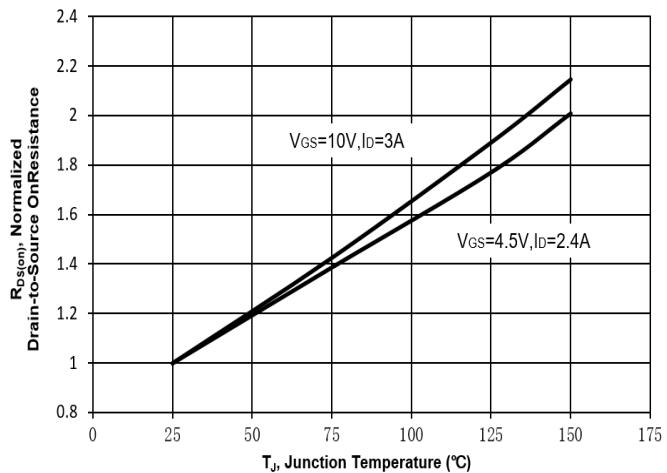
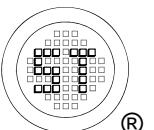
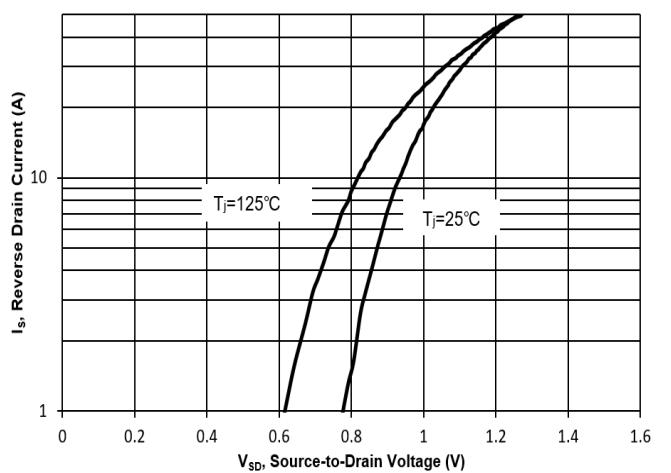


Fig. 6 Typical Body-Diode Forward Characteristics



# MMFTN2362

## Electrical Characteristics Curves

Fig. 7 Typical Junction Capacitance

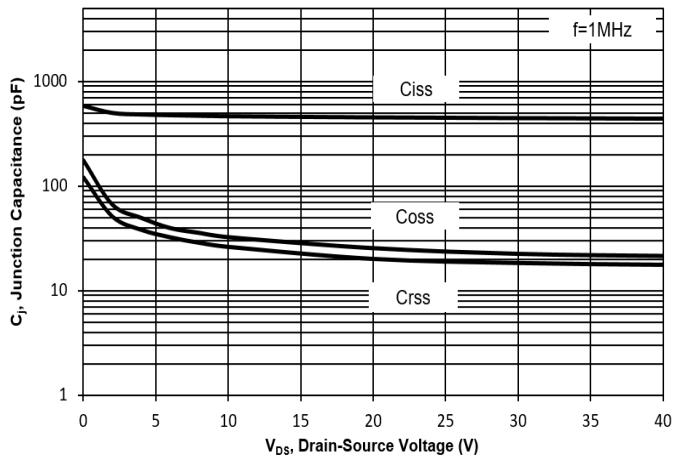


Fig. 8 Drain-Source Leakage Current vs.  $T_j$

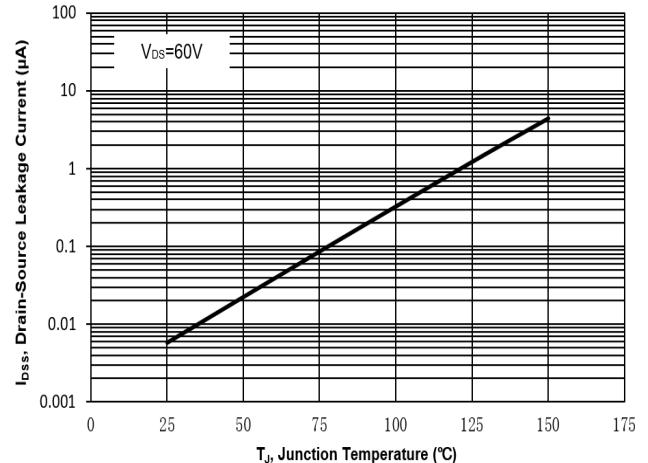


Fig. 9  $V_{(BR)DSS}$  vs. Junction Temperature

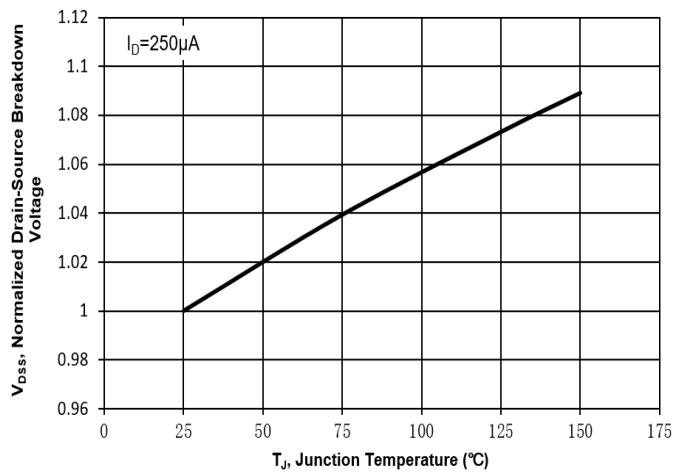


Fig. 10 Gate Threshold Variation vs.  $T_j$

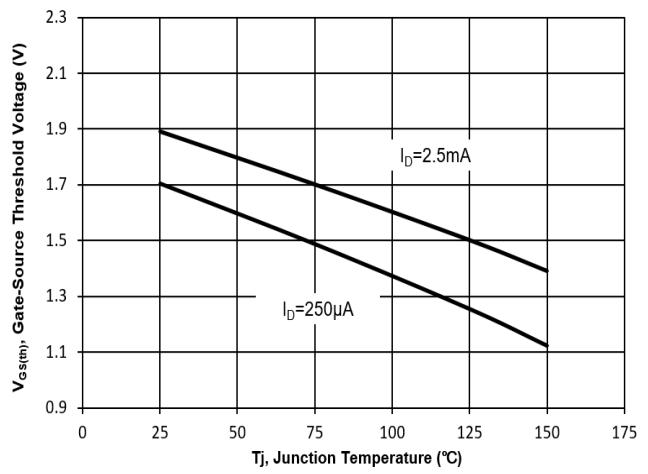
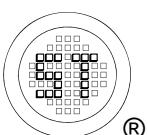
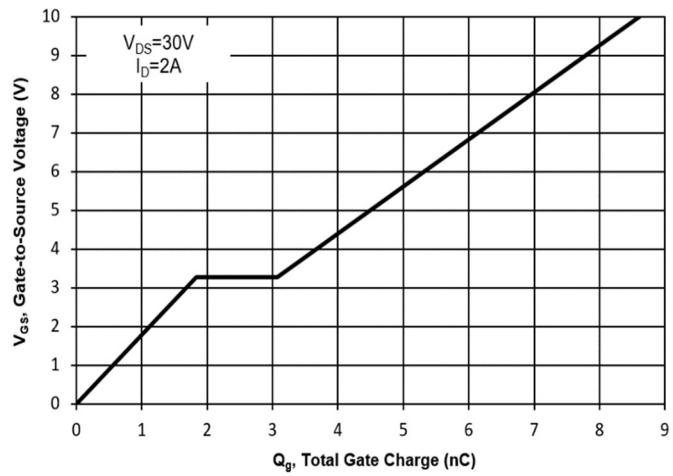


Fig. 11 Gate Charge



## Test Circuits

Fig.1-1 Switching times test circuit

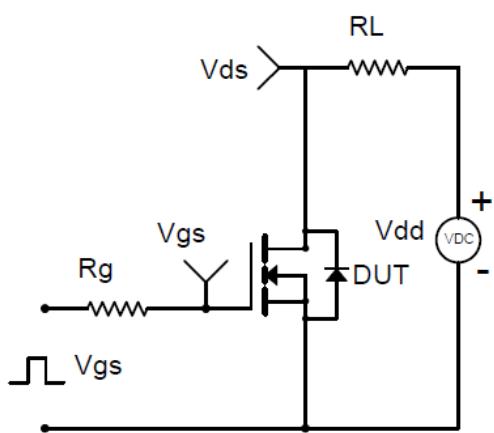


Fig.1-2 Switching Waveform

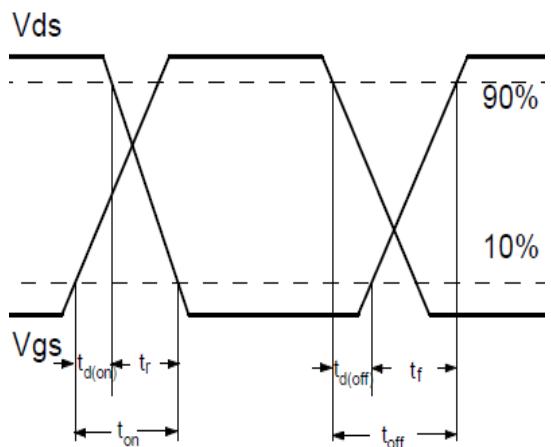


Fig.2-1 Gate charge test circuit

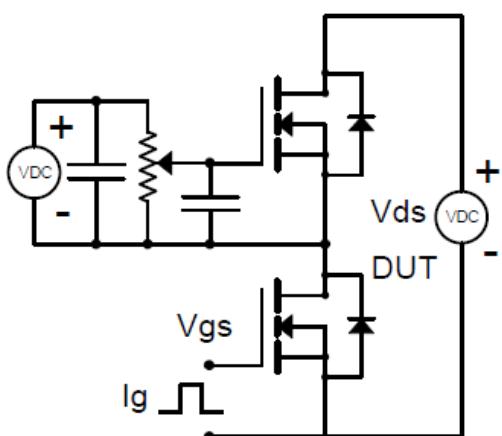
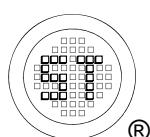
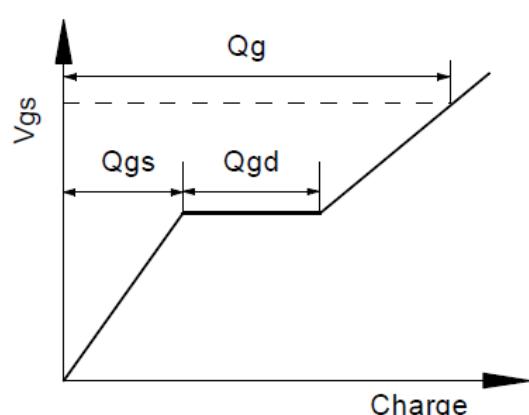


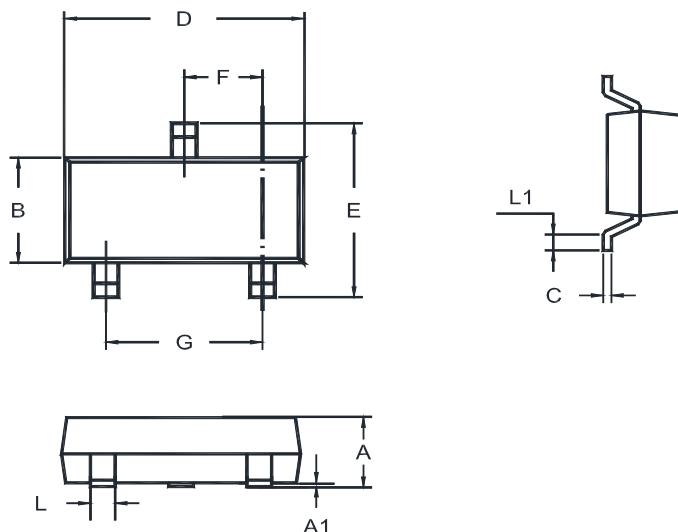
Fig.2-2 Gate charge waveform



# MMFTN2362

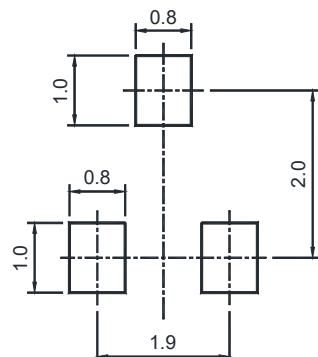
## Package Outline (Dimensions in mm)

SOT-23



Unit	A	A1	B	C	D	E	F	G	L	L1
mm	1.20 0.89	0.100 0.013	1.40 1.20	0.19 0.08	3.04 2.80	2.6 2.2	1.02 0.89	2.04 1.78	0.51 0.37	0.2 MIN

## Recommended Soldering Footprint



## Packing information

Package	Tape Width (mm)	Pitch		Reel Size		Per Reel Packing Quantity
		mm	inch	mm	inch	
SOT-23	8	4 ± 0.1	0.157 ± 0.004	178	7	3,000

## Marking information

" M4 " = Part No.

" YM " = Date Code Marking

" Y " = Year

" M " = Month

Font type: Arial

