

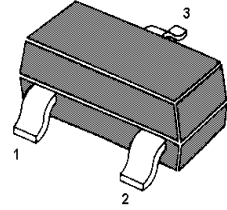
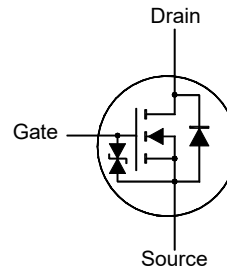
MKA10N560LK

N-Channel Enhancement Mode MOSFET

Features

- Surface-mounted package
- Advanced trench cell design
- Built-in G-S Protection Diode
- Typical ESD Protection HBM Class 1C

Classification	Voltage Range(V)
0A	< 125
0B	125 to < 250
1A	250 to < 500
1B	500 to < 1000
1C	1000 to < 2000
2	2000 to < 4000
3A	4000 to < 8000
3B	≥ 8000



1. Gate 2. Source 3. Drain
SOT-23 Plastic Package

Applications

- Portable appliances
- Battery management
- High speed switch
- Low power DC to DC Converter

Absolute Maximum Ratings (at $T_a = 25^\circ\text{C}$ unless otherwise specified)

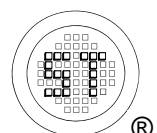
Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DS}	100	V
Gate-Source Voltage	V_{GS}	± 20	V
Drain Current	I_D	1	A
Peak Drain Current, Pulsed ¹⁾	I_{DM}	4	A
Power Dissipation ²⁾	P_D	1	W
Operating Junction Temperature	T_J	150	$^\circ\text{C}$
Storage Temperature Range	T_{stg}	- 55 to + 150	$^\circ\text{C}$

Thermal Resistance Ratings

Parameter	Symbol	Max.	Unit
Thermal Resistance from Junction to Ambient ²⁾	$R_{\theta JA}$	125	$^\circ\text{C/W}$

¹⁾ Pulse Test: Pulse Width $\leq 100 \mu\text{s}$, Duty Cycle $\leq 2\%$, Repetitive rating, pulse width limited by junction temperature $T_{J(MAX)}=150^\circ\text{C}$.

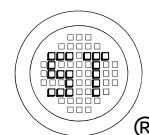
²⁾ Device mounted on FR-4 substrate PC board, 2oz copper, with 1-inch square copper plate in still air.



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Characteristics at $T_a = 25^\circ\text{C}$ unless otherwise specified

Parameter	Symbol	Min.	Typ.	Max.	Unit
STATIC PARAMETERS					
Drain-Source Breakdown Voltage at $I_D = 1\text{ mA}$	$V_{(BR)DSS}$	100	-	-	V
Zero Gate Voltage Drain Current at $V_{DS} = 100\text{ V}$	I_{DSS}	-	-	1	μA
Gate-Source Leakage at $V_{GS} = \pm 20\text{ V}$	I_{GSS}	-	-	± 10	μA
Gate-Source Threshold Voltage at $V_{DS} = V_{GS}$, $I_D = 250\text{ }\mu\text{A}$	$V_{GS(th)}$	1	-	2.5	V
Drain-Source On-State Resistance at $V_{GS} = 10\text{ V}$, $I_D = 1\text{ A}$ at $V_{GS} = 4.5\text{ V}$, $I_D = 1\text{ A}$ at $V_{GS} = 4\text{ V}$, $I_D = 1\text{ A}$	$R_{DS(on)}$	- - -	- - -	520 560 580	$\text{m}\Omega$
DYNAMIC PARAMETERS					
Forward Transfer Admittance at $V_{DS} = 10\text{ V}$, $I_D = 1\text{ A}$	g_{FS}	-	2.3	-	S
Input Capacitance at $V_{DS} = 30\text{ V}$, $V_{GS} = 0\text{ V}$, $f = 1\text{ MHz}$	C_{iss}	-	438	-	pF
Output Capacitance at $V_{DS} = 30\text{ V}$, $V_{GS} = 0\text{ V}$, $f = 1\text{ MHz}$	C_{oss}	-	15	-	pF
Reverse Transfer Capacitance at $V_{DS} = 30\text{ V}$, $V_{GS} = 0\text{ V}$, $f = 1\text{ MHz}$	C_{rss}	-	11	-	pF
Total Gate Charge at $V_{DS} = 50\text{ V}$, $I_D = 1\text{ A}$, $V_{GS} = 10\text{ V}$ at $V_{DS} = 50\text{ V}$, $I_D = 1\text{ A}$, $V_{GS} = 4.5\text{ V}$	Q_g	- -	9.5 4.2	- -	nC
Gate-Source Charge at $V_{DS} = 50\text{ V}$, $I_D = 1\text{ A}$, $V_{GS} = 10\text{ V}$	Q_{gs}	-	1.9	-	nC
Gate-Drain Charge at $V_{DS} = 50\text{ V}$, $I_D = 1\text{ A}$, $V_{GS} = 10\text{ V}$	Q_{gd}	-	1.2	-	nC
Turn-On Delay Time at $V_{DS} = 50\text{ V}$, $I_D = 1\text{ A}$, $V_{GS} = 10\text{ V}$, $R_G = 3.9\text{ }\Omega$	$t_{d(on)}$	-	3.9	-	ns
Turn-On Rise Time at $V_{DS} = 50\text{ V}$, $I_D = 1\text{ A}$, $V_{GS} = 10\text{ V}$, $R_G = 3.9\text{ }\Omega$	t_r	-	2.5	-	ns
Turn-Off Delay Time at $V_{DS} = 50\text{ V}$, $I_D = 1\text{ A}$, $V_{GS} = 10\text{ V}$, $R_G = 3.9\text{ }\Omega$	$t_{d(off)}$	-	15.7	-	ns
Turn-Off Fall Time at $V_{DS} = 50\text{ V}$, $I_D = 1\text{ A}$, $V_{GS} = 10\text{ V}$, $R_G = 3.9\text{ }\Omega$	t_f	-	15.2	-	ns
Body-Diode PARAMETERS					
Drain-Source Diode Forward Voltage at $V_{GS} = 0\text{ V}$, $I_S = 1\text{ A}$	V_{SD}	-	-	1.2	V
Body-Diode Continuous Current	I_S	-	-	1	A
Body Diode Reverse Recovery Time at $I_S = 1\text{ A}$, $di/dt = 100\text{ A}/\mu\text{s}$	t_{rr}	-	15	-	ns
Body Diode Reverse Recovery Charge at $I_S = 1\text{ A}$, $di/dt = 100\text{ A}/\mu\text{s}$	Q_{rr}	-	10	-	nC



Electrical Characteristics Curves

Fig. 1 Typical Output Characteristics

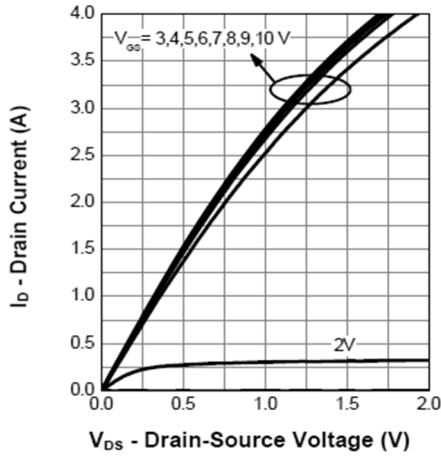


Fig. 2 on-Resistance vs. Gate-Source Voltage

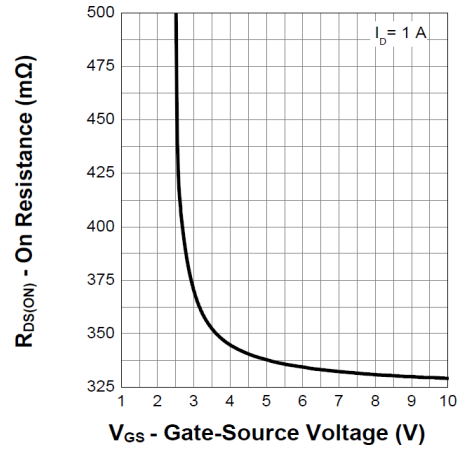


Fig. 3 On-Resistance vs. Drain Current

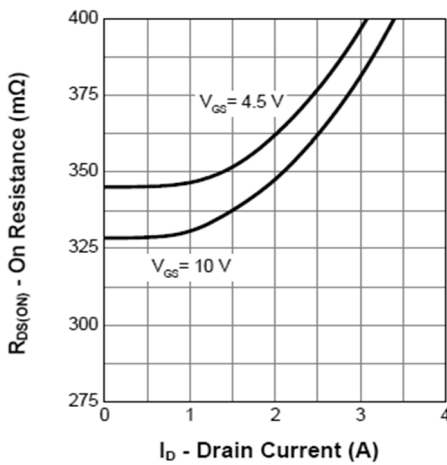


Fig. 4 Gate Threshold Variation vs. Tj

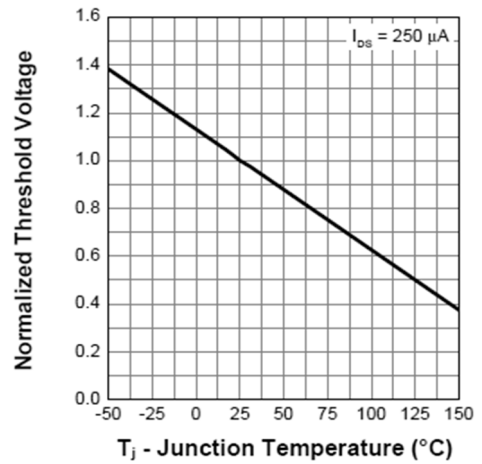


Fig. 5 on-Resistance vs. Tj

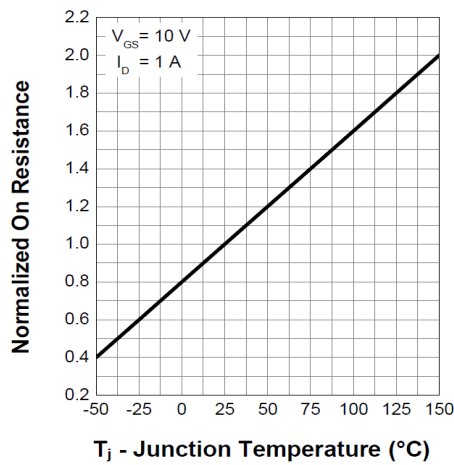
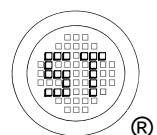
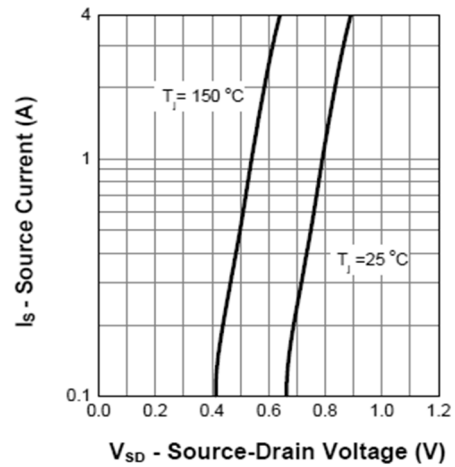


Fig. 6 Typical Forward Characteristics



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Electrical Characteristics Curves

Fig. 7 Typical Junction Capacitance

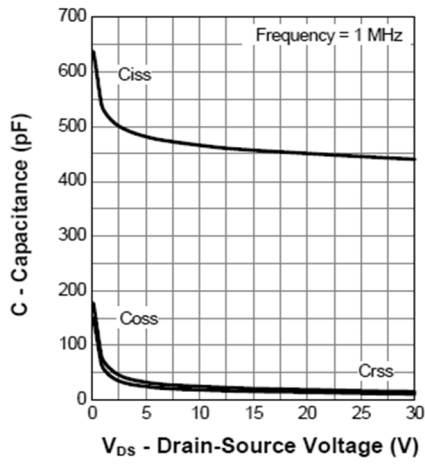
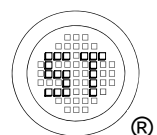
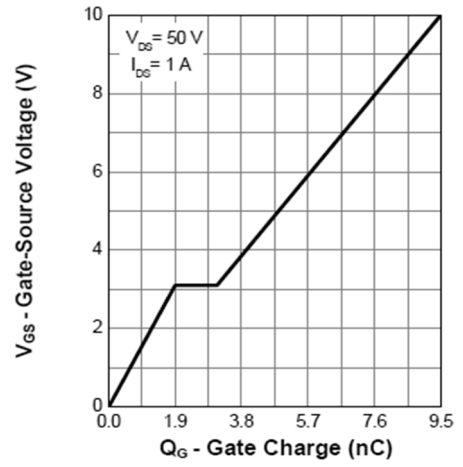


Fig. 8 Gate Charge



Test Circuits

Fig.1-1 Switching times test circuit

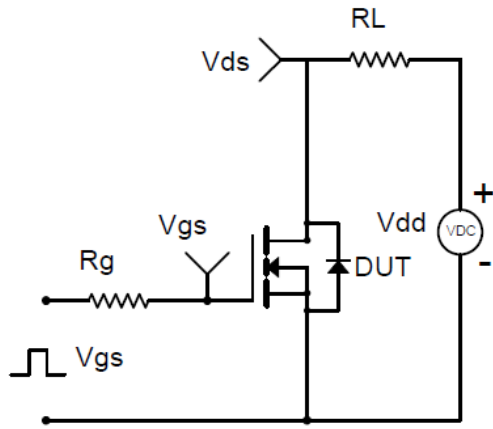


Fig.1-2 Switching Waveform

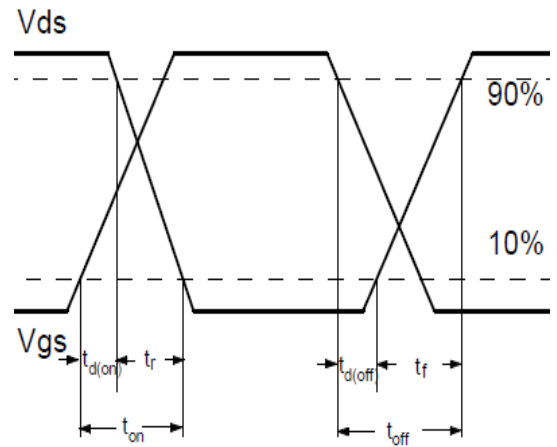


Fig.2-1 Gate charge test circuit

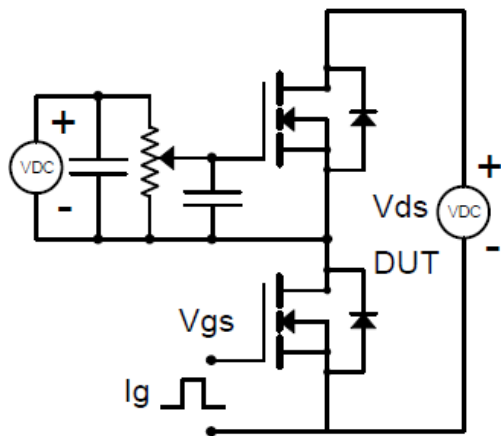
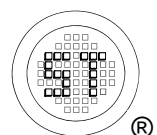
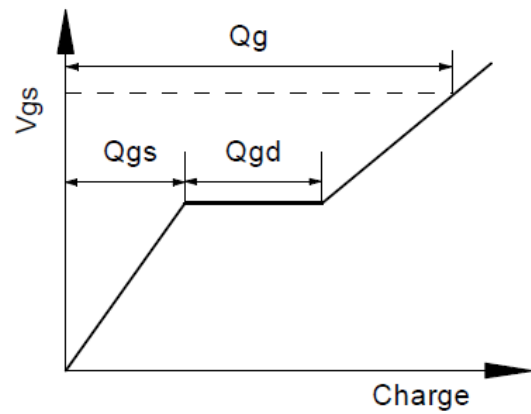


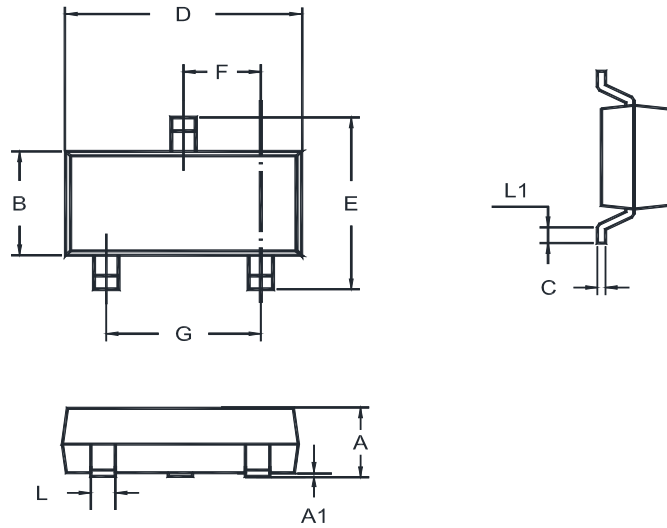
Fig.2-2 Gate charge waveform



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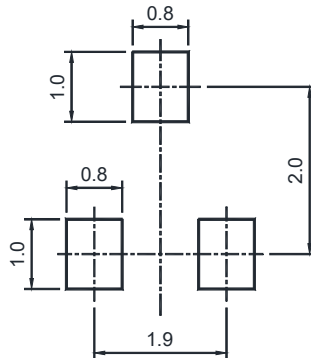
Package Outline (Dimensions in mm)

SOT-23



Unit	A	A1	B	C	D	E	F	G	L	L1
mm	1.20	0.100	1.40	0.19	3.04	2.6	1.02	2.04	0.51	0.2
	0.89	0.013	1.20	0.08	2.80	2.2	0.89	1.78	0.37	MIN

Recommended Soldering Footprint



Packing information

Package	Tape Width (mm)	Pitch		Reel Size		Per Reel Packing Quantity
		mm	inch	mm	inch	
SOT-23	8	4 ± 0.1	0.157 ± 0.004	178	7	3,000

Marking information

- " TM1 " = Part No.
 - " YM " = Date Code Marking
 - " Y " = Year
 - " M " = Month
- Font type: Arial

