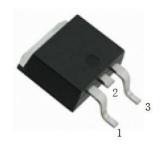
## MJD122R-HAF

# **NPN Silicon Power Darlington Transistor**

### **Features**

• Halogen and Antimony Free(HAF), RoHS compliant



1.Base 2.Collector 3.Emitter TO-252 Plastic Package

#### **Absolute Maximum Ratings**

Parameter	Symbol	Rating	Unit	
Collector Base Voltage	V <sub>CBO</sub>	100	V	
Collector Emitter Voltage	V <sub>CEO</sub>	100	V	
Emitter Base Voltage	V <sub>EBO</sub>	V <sub>EBO</sub> 5		
Collector Current	Ic	8	Α	
Peak Collector Current	I <sub>CM</sub>	16	Α	
Base Current	I <sub>B</sub>	120	mA	
Total Power Dissipation	T <sub>C</sub> = 25 °C	P <sub>tot</sub>	20	W
Total Power Dissipation 1)	P <sub>tot</sub>	1.75	W	
Operating Junction and Storage Temper	$T_j, T_{stg}$	- 65 to + 150	°C	

#### **Thermal Characteristics**

Parameter	Symbol	Max.	Unit	
Thermal Resistance from Junction to Case	R <sub>0</sub> JC	6.25	°C/W	
Thermal Resistance from Junction to Ambient 1)	Reja	71.4	°C/W	

<sup>&</sup>lt;sup>1)</sup> Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.



# MJD122R-HAF

## Characteristics at $T_j = 25^{\circ}C$ unless otherwise specified

Parameter	Symbol	Min.	Max.	Unit
DC Current Gain at $V_{CE} = 4 \text{ V}$ , $I_C = 4 \text{ A}$ at $V_{CE} = 4 \text{ V}$ , $I_C = 8 \text{ A}$	h <sub>FE</sub>	1000 100	12000	
Collector Base Breakdown Voltage at I <sub>C</sub> = 100 μA	V <sub>(BR)CBO</sub>	100	-	V
Collector Emitter Breakdown Voltage at I <sub>C</sub> = 30 mA	V <sub>(BR)CEO</sub>	100	-	V
Emitter Base Breakdown Voltage at $I_E = 1 \text{ mA}$	V <sub>(BR)EBO</sub>	5	-	V
Collector Emitter Cutoff Current at V <sub>CE</sub> = 50 V	I <sub>CEO</sub>	-	10	μA
Collector Base Cutoff Current at V <sub>CB</sub> = 100 V	Ісво	-	10	μA
Emitter Base Cutoff Current at $V_{EB} = 5 \text{ V}$	ІЕВО	-	2	mA
Collector Emitter Saturation Voltage at $I_C = 4$ A, $I_B = 16$ mA at $I_C = 8$ A, $I_B = 80$ mA	V <sub>CE(sat)</sub>	- -	2 4	V
Base Emitter Saturation Voltage at $I_C = 8 \text{ A}$ , $I_B = 80 \text{ mA}$	V <sub>BE(sat)</sub>	-	4.5	V
Base-Emitter On Voltage at $V_{CE} = 4 \text{ V}$ , $I_C = 4 \text{ A}$	V <sub>BE(on)</sub>	-	2.8	V
Current Gain Bandwidth Product at $V_{CE} = 4 \text{ V}$ , $I_C = 3 \text{ A}$ , $f = 1 \text{ MHz}$	f⊤	4	-	MHz
Collector Output Capacitance at $V_{CB} = 10 \text{ V}$ , $I_E = 0$ , $f = 0.1 \text{ MHz}$	Cob	-	200	pF



#### **Electrical Characteristics Curves**

Fig. 1 Output Characteristics Curve

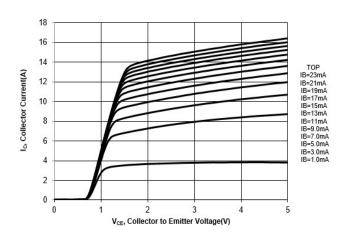


Fig. 2 Collector Current vs. Base to Emitter Voltage

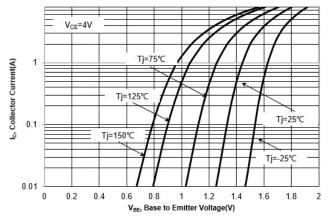


Fig. 3 DC Current Gain vs. Collector Current

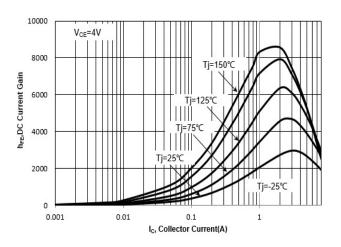
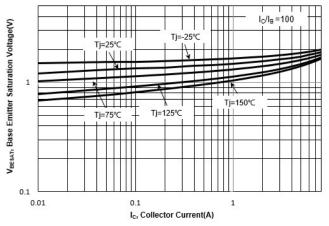


Fig. 4 V<sub>BESAT</sub> vs. Collector Current



#### **Electrical Characteristics Curves**

Fig. 5  $V_{\text{CESAT}}$  vs. Collector Current

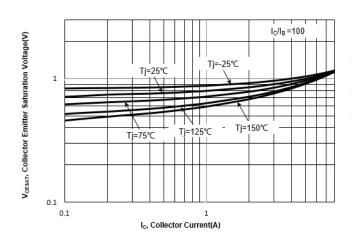


Fig. 6 Output Capacitance

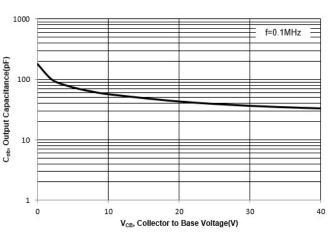
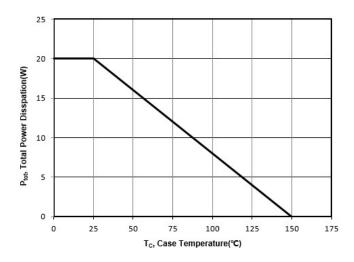


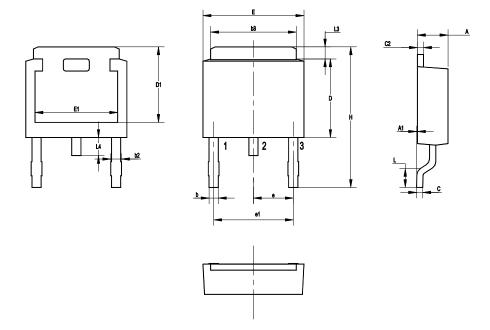
Fig. 7 Power Derating Curve





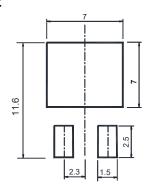
# Package Outline (Dimensions in mm)

TO-252



	UNIT	Α	A1	b	b2	b3	С	C2	D	D1	Е	E1	е	e1	Н	L	L3	L4
Ī		2.5	0.15	1.0	1.15	5.5	0.65	0.65	6.2	5.4	6.7	5.0	2.30	4.60	10.7	1.78	1.20	1.10
	mm	2.1	0	0.5	0.65	4.9	0.4	0.4	5.6	5.0	6.1	4.6	TYP.	TYP.	9	1.40	0.85	0.51

### **Recommended Soldering Footprint**



#### Packing information

	i doking iino	1111411011					
	Package	Tape Width	dth		Reel	Size	Per Reel Packing Quantity
		(mm)	mm	inch	mm	inch	- Fel Neel Facking Qualitity
	TO-252	16	8 ± 0.1	0.315 ± 0.004	330	13	2,500

### **Marking information**

" MJD122R " = Part No.

" \*\*\*\*\* " = Date Code Marking

Font type: Arial



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