

ESDUL36WT

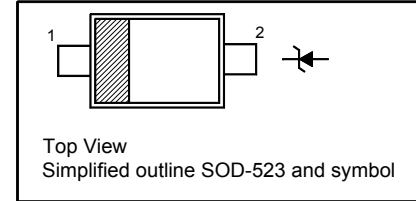
ESD Protection Diode

Features

- Protects one I/O line
- Low leakage current
- Low clamping voltage

PINNING

PIN	DESCRIPTION
1	Cathode
2	Anode



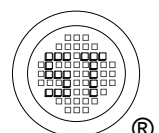
Absolute Maximum Ratings ($T_a = 25^\circ\text{C}$)

Parameter	Symbol	Value	Unit
Peak Pulse Power ($t_p = 8/20 \mu\text{s}$)	P_{PK}	120	W
Peak Pulse Current ($t_p = 8/20 \mu\text{s}$)	I_{PP}	2	A
IEC61000-4-2 (ESD) Air Contact	V_{ESD}	± 25 ± 25	KV
Operation Junction Temperature Range	T_j	- 55 to + 125	$^\circ\text{C}$
Storage Temperature Range	T_{stg}	- 55 to + 150	$^\circ\text{C}$

Characteristics at $T_a = 25^\circ\text{C}$

Parameter	Symbol	Min.	Typ.	Max.	Unit
Reverse Stand-Off Voltage	V_{RWM}	-	-	36	V
Reverse Breakdown Voltage at $I_R = 1 \text{ mA}$	$V_{(BR)R}$	40	-	-	V
Reverse Current at $V_{RWM} = 36 \text{ V}$	I_R	-	-	1	μA
Clamping Voltage at $I_{PP} = 0.8 \text{ A}$, $t_p = 8/20 \mu\text{s}$	V_C	-	-	57	V
ESD Clamping Voltage at $I_{TLP} = 4 \text{ A}$, $t_p = 0.2/100 \text{ ns}$ at $I_{TLP} = 16 \text{ A}$, $t_p = 0.2/100 \text{ ns}$	V_{CL}	-	49.5 54.7	-	V
Junction Capacitance at $V_R = 0 \text{ V}$, $f = 1 \text{ MHz}$	C_j	-	35	-	pF
Dynamic Resistance ¹⁾	R_{dyn}	-	0.43	-	Ω

¹⁾ Dynamic Resistance calculated from $I_{TLP} = 4 \text{ A}$ to $I_{TLP} = 16 \text{ A}$.



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Electrical Characteristic Curves

Fig 1. Pulse Waveform

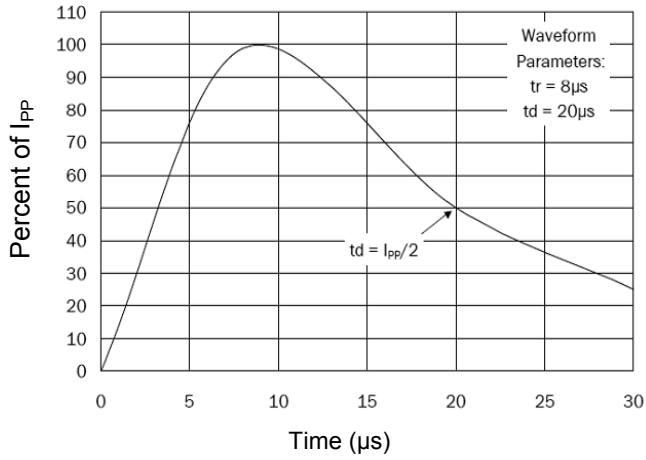


Fig 2. Power Derating Curve

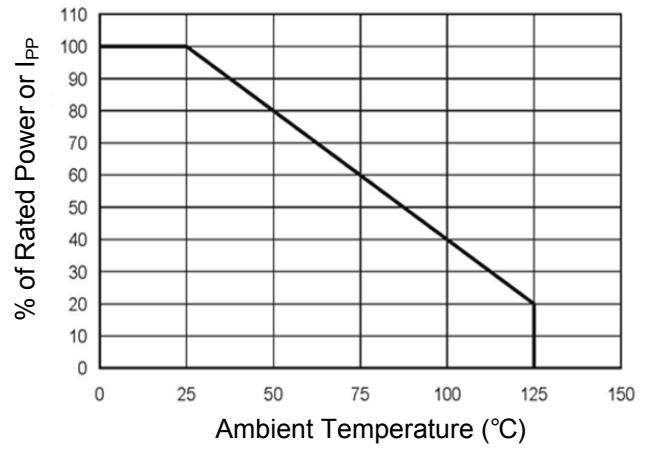


Fig 3. Clamping Voltage Curve

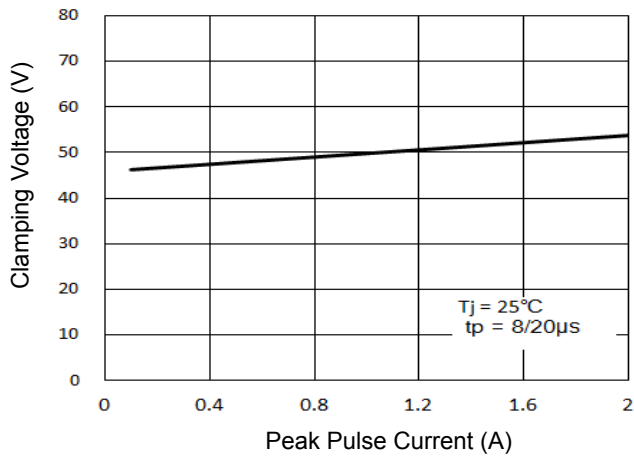


Fig4. Junction Capacitance

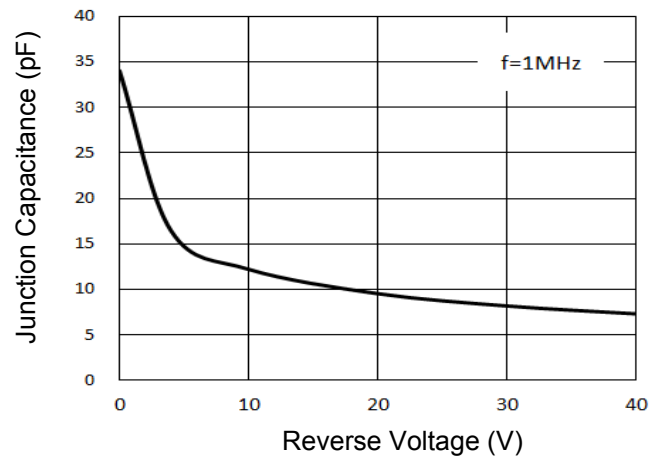
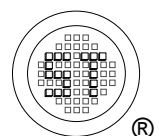
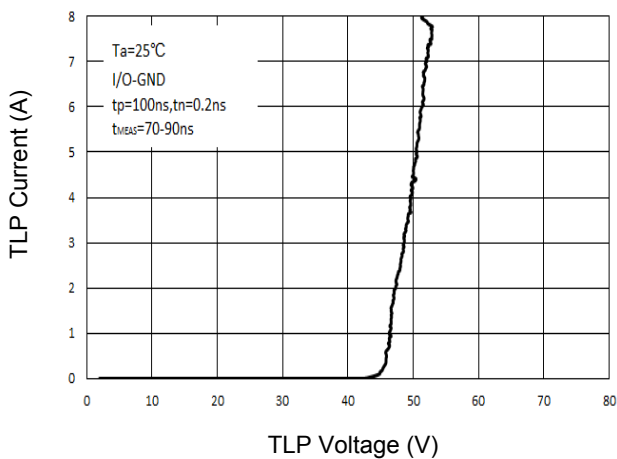


Fig. 5 TLP Curve

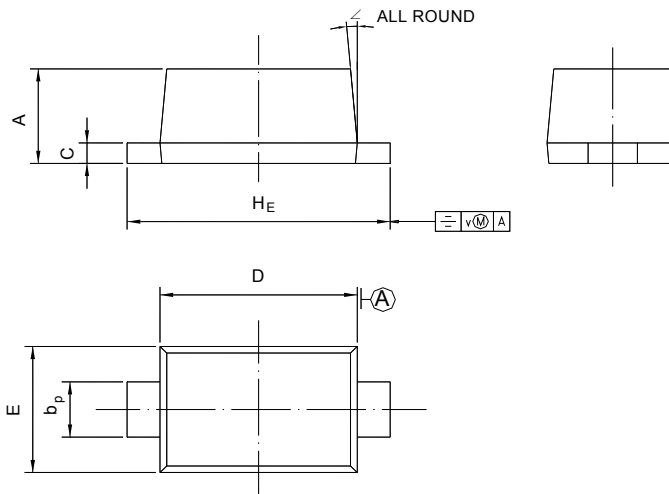


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PACKAGE OUTLINE

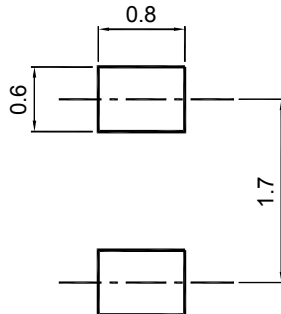
Plastic surface mounted package; 2 leads

SOD-523



UNIT	A	b _p	C	D	E	H _E	V	∠
mm	0.70 0.60	0.4 0.3	0.135 0.100	1.25 1.15	0.85 0.75	1.7 1.5	0.1	5°

Recommended Soldering Footprint



Packing information

Package	Tape Width (mm)	Pitch		Reel Size		Per Reel Packing Quantity
		mm	inch	mm	inch	
SOD-523	8	4 ± 0.1	0.157 ± 0.004	178	7	4,000

Marking information

" L6 " = Part No.

" III " = Cathode line

Font type: Arial

