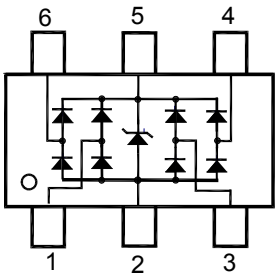
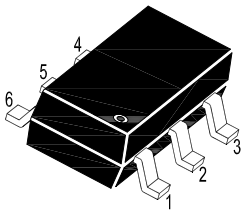
ESDLC4201D

ESD Protection Diode





1. I/O1 2. GND 3. I/O2
4. I/O3 5. V_{CC} 6. I/O4
Marking Code: **63M**
SOT-26 Plastic package

Absolute Maximum Ratings (T_a = 25℃)

Parameter	Symbol	Value	Unit
Peak Power Dissipation (tp = 8/20 μs)	P_{PK}	500	W
Peak Pulse Current (tp = 8/20 μs)	I_{PP}	25	A
ESD per IEC 61000-4-2 (Air) ESD per IEC 61000-4-2 (Contact)	V_{PP}	20 20	KV
IEC 61000−4−4 (5/50 ns)	EFT	40	A
Operating junction Temperature Range	T_j	- 40 to + 125	℃
Storage Temperature Range	T_{stg}	- 55 to + 150	℃

Characteristics at T_a = 25℃

Parameter	Symbol	Min.	Max.	Unit
Reverse Voltage at Pin 5 to 2	V_{RWM}	-	5	V
Reverse Breakdown Voltage at I_T = 1 mA	V_{BR}	6	-	V
Leakaeg Current at V_R = 5 V	I_R	-	5	μA
Clamping Voltage at I_{PP} = 5 A, tp = 8/20 μs, Pin 5 to Pin 2 at I_{PP} = 8 A, tp = 8/20 μs, Pin 5 to Pin 2 at I_{PP} = 25 A, tp = 8/20 μs, Pin 5 to Pin 2	V_C	- - -	11 12 20	V
Junction Capacitance at V_R = 0 V, f = 1 MHz, Between I/O Pins to GND at V_R = 0 V, f = 1 MHz, Between I/O Pins	C_j	- -	5 3	pF

Dated: 01/02/2021 Rev: 04

Electrical characteristic curves

Fig 1. Pulse Waveform

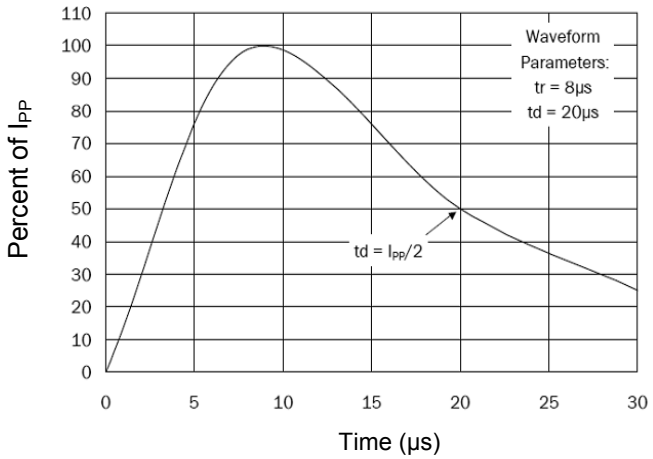


Fig 2. Power Derating Curve

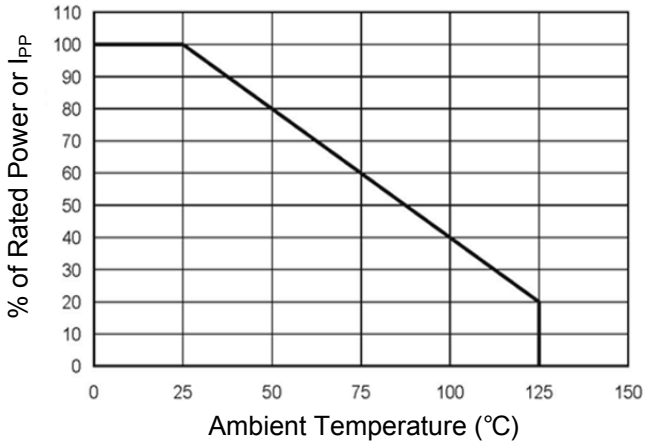


Fig 3. Clamping Voltage Curve

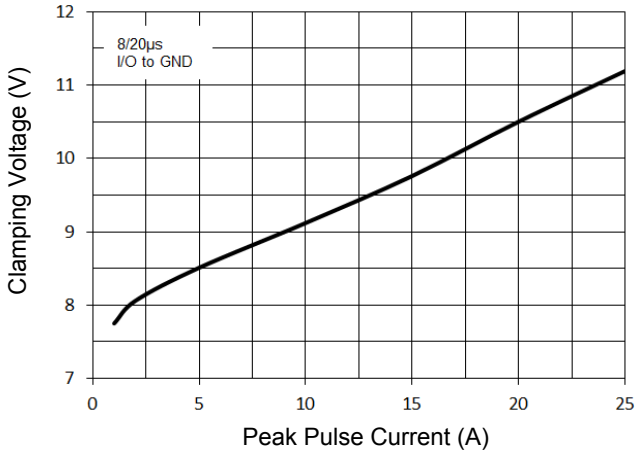


Fig 4. Junction Capacitance

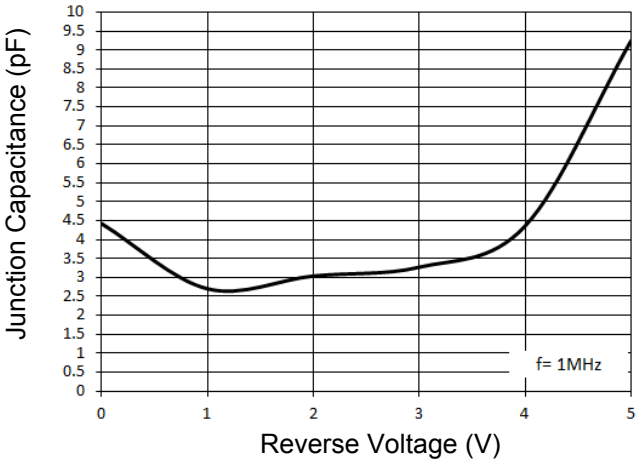
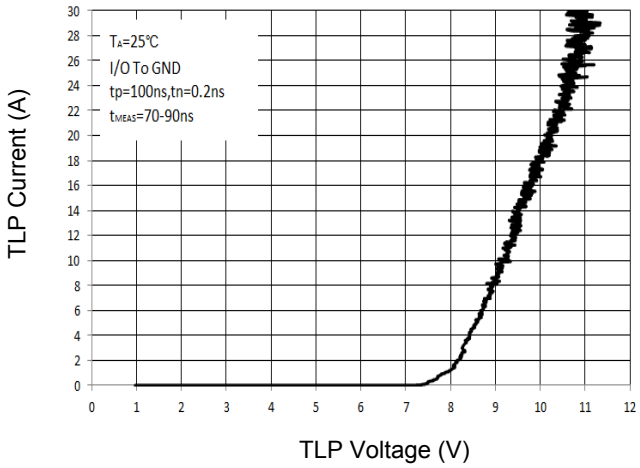
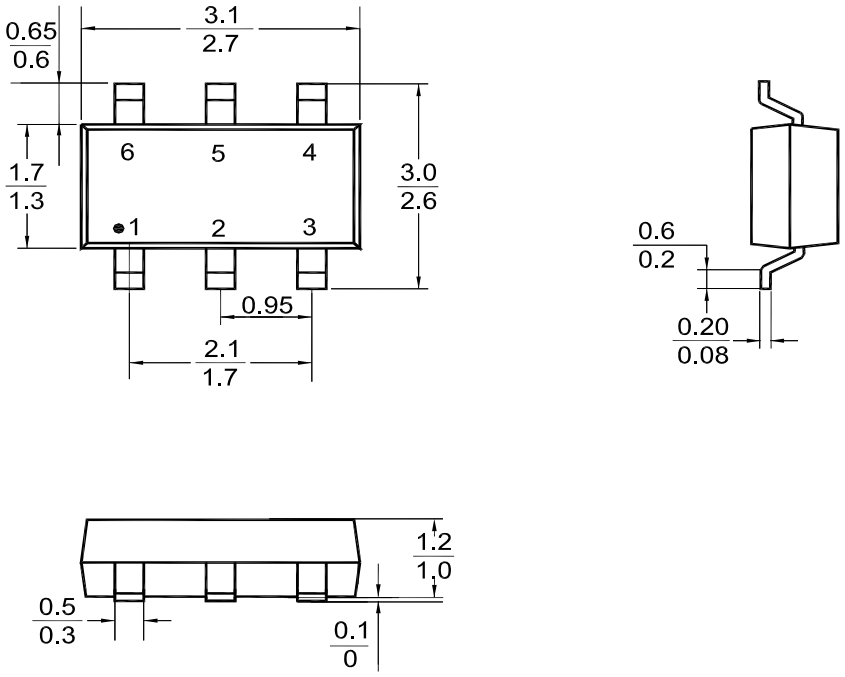


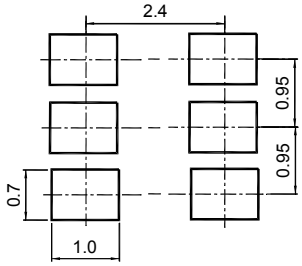
Fig. 5 TLP Curve



Package Outline Dimensions (Units: mm) SOT-26



Recommended Soldering Footprint



Packing information

Package	Tape Width (mm)	Pitch		Reel Size		Per Reel Packing Quantity
		mm	inch	mm	inch	
SOT-26	8	4 ± 0.1	0.157 ± 0.004	178	7	3,000

Marking information

" 63M" = Part No.
"YM" = Date Code Marking
"Y" = Year
"M" = Month
Font type: Arial