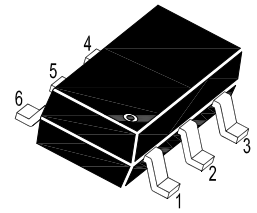
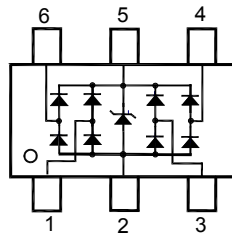


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ESD Protection Diode



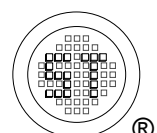
1. I/O1 2. GND 3. I/O2
4. I/O3 5. V_{CC} 6. I/O4
Marking Code: F54
SOT-26 Plastic package

Absolute Maximum Ratings (T_a = 25°C)

Parameter	Symbol	Value	Unit
Peak Pulse Power (tp = 8/20 μs)	P _{PK}	150	W
Peak Pulse Current (tp = 8/20 μs)	I _{PP}	6	A
ESD per IEC 61000-4-2 (Air) ESD per IEC 61000-4-2 (Contact)	V _{ESD}	± 15 ± 8	KV
Operation Temperature Range	T _J	- 55 to + 125	°C
Storage Temperature Range	T _{stg}	- 55 to + 150	°C

Characteristics at T_a = 25°C

Parameter	Symbol	Min.	Max.	Unit
Reverse Stand-Off Voltage at Pin 5 to 2	V _{RWM}	-	5	V
Reverse Breakdown Voltage at I _t = 1 mA, Pin 5 to 2	V _{(BR)R}	6	-	V
Reverse Current at V _{RWM} = 5 V, Pin 5 to 2	I _R	-	3	μA
Clamping Voltage at I _{PP} = 1 A, tp = 8/20 μs, Any Pin to Pin 2 at I _{PP} = 6 A, tp = 8/20 μs, Any Pin to Pin 2	V _C	-	15 25	V
Junction Capacitance at V _R = 0 V, f = 1 MHz, Any I/O Pin to Pin 2 at V _R = 0 V, f = 1 MHz, Between I/O Pins	C _j	-	3 1.5	pF



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Electrical characteristic curves

Fig 1. Pulse Waveform

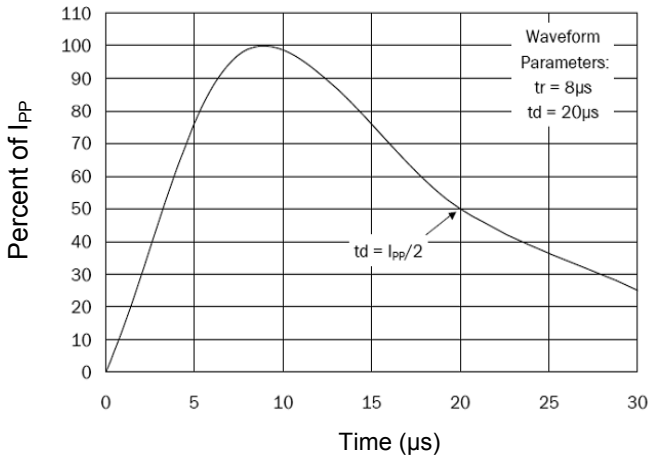


Fig 2. Power Derating Curve

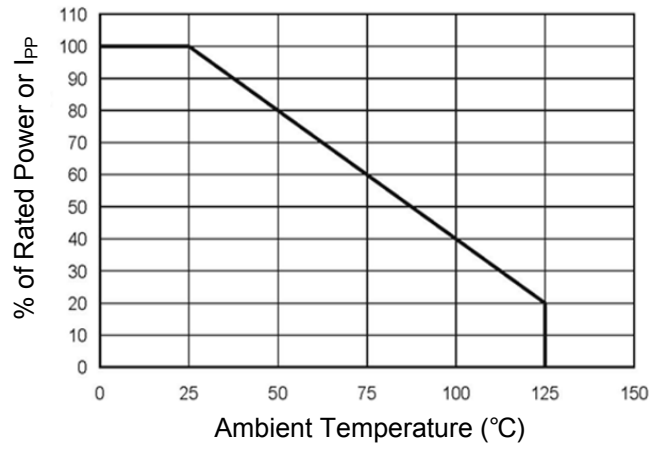


Fig 3. Clamping Voltage Curve

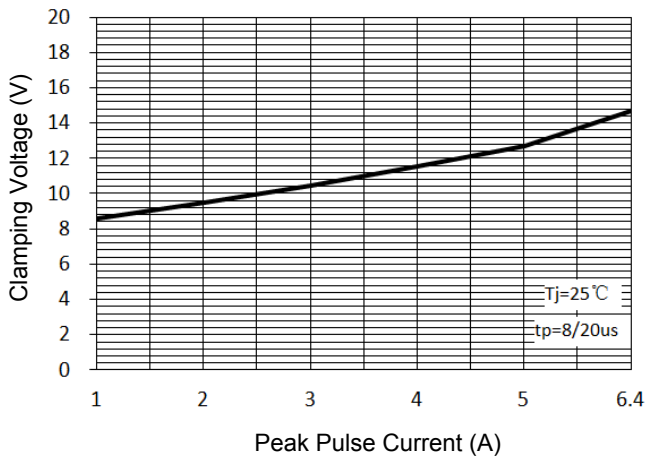


Fig 4. Junction Capacitance

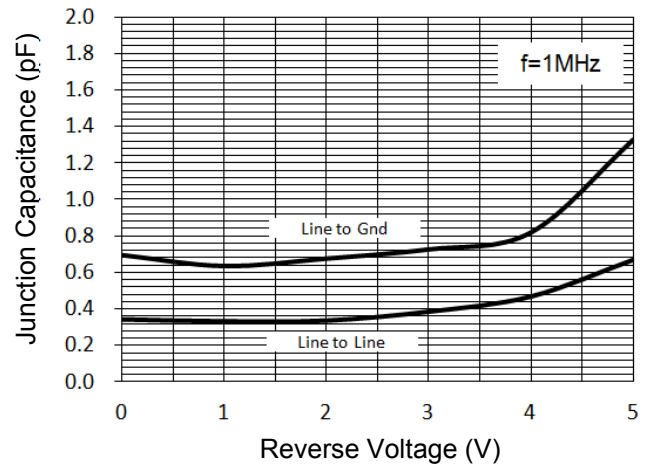
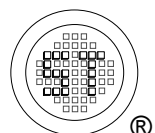
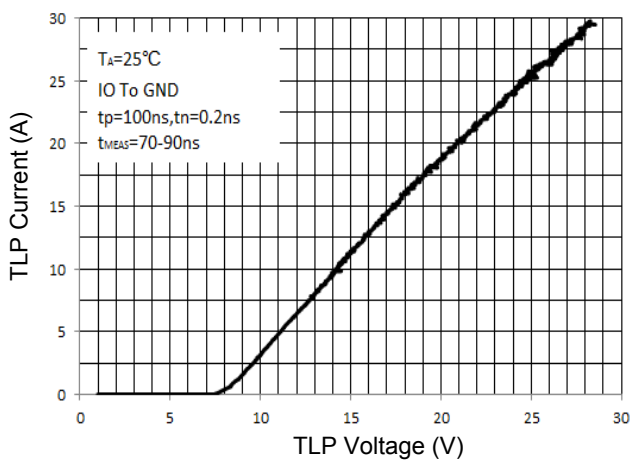


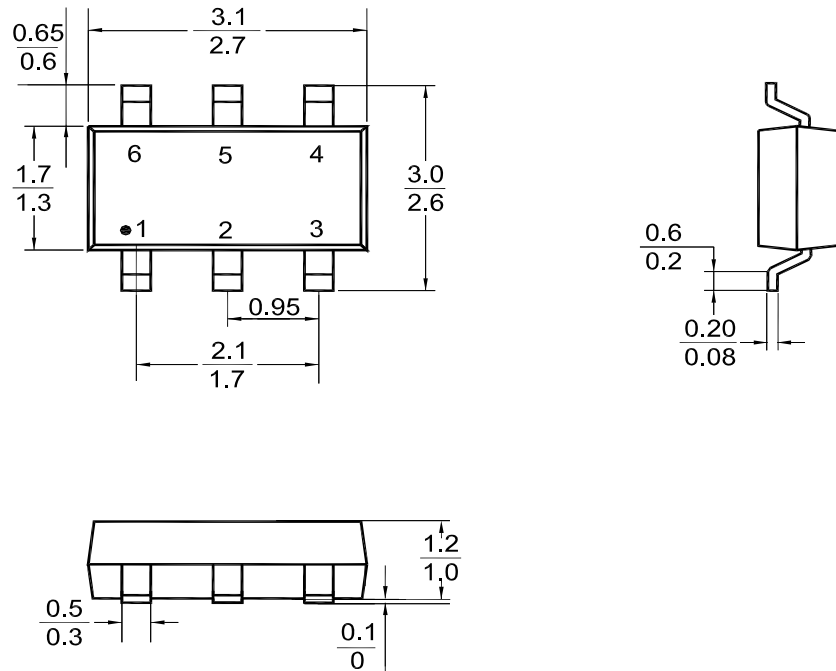
Fig 5 TLP Curve



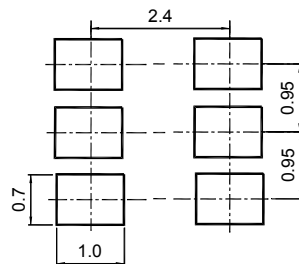
ESDLC0504D

Package Outline Dimensions (Units: mm)

SOT-26



Recommended Soldering Footprint



Packing information

Package	Tape Width (mm)	Pitch		Reel Size		Per Reel Packing Quantity
		mm	inch	mm	inch	
SOT-26	8	4 ± 0.1	0.157 ± 0.004	178	7	3,000

Marking information

"F54" = Part No.
 "YM" = Date Code Marking
 "Y" = Year
 "M" = Month
 Font type: Arial

