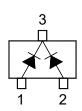
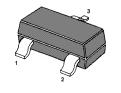
# **BAV70CC-AH**

# Silicon Epitaxial Planar Switching Diode

#### **Features**

- AEC-Q101 Qualified
- Small package
- · Low forward voltage
- Fast reverse recovery time
- Small total capacitance
- Halogen and Antimony Free(HAF), RoHS compliant





1. Anode 2. Anode 3. Cathode SOT-23 Plastic Package

### **Applications**

• Ultra high speed switching application

### Absolute Maximum Ratings (T<sub>a</sub> = 25 ∘C)

Parameter	Symbol	Value	Unit	
Maximum Peak Reverse Voltage	$V_{RM}$	100	V	
Reverse Voltage	V <sub>R</sub>	75	V	
Average Forward Current	I <sub>F(AV)</sub>	I <sub>F(AV)</sub> 200		
Maximum Peak Forward Current	I <sub>FM</sub>	300	mA	
Non-Repetitive Peak Forward Surge Current	at t = 1 s at t = 1 µs	IFSM	1 2	А
Power Dissipation		P <sub>D</sub>	350	mW
Junction Temperature		Tj	150	°C
Storage Temperature Range		T <sub>stg</sub>	- 55 to + 150	°C

### **Thermal Resistance**

Parameter	Symbol	Max.	Unit
Thermal Resistance from Junction to Ambient 1)	$R_{\theta JA}$	357	°C/W

<sup>1)</sup> Device mounted on FR-4 substrate PC board, with minimum recommended pad layout.



# **BAV70CC-AH**

### Characteristics at T<sub>a</sub> = 25 ∘C

Parameter	Symbol	Min.	Max.	Unit
Forward Voltage				
at I <sub>F</sub> = 1 mA	VF	-	715	mV
at I <sub>F</sub> = 10 mA	VF	-	855	mV
at I <sub>F</sub> = 50 mA	VF	-	1	V
at I <sub>F</sub> = 150 mA	V <sub>F</sub>	-	1.25	V
Reverse Current				
at V <sub>R</sub> = 20 V	$I_R$	-	25	nA
at V <sub>R</sub> = 75 V	IR	-	2.5	μA
at V <sub>R</sub> = 25 V, T <sub>J</sub> = 150 °C	I <sub>R</sub>	-	30	μA
at V <sub>R</sub> = 75 V, T <sub>J</sub> = 150 °C	I <sub>R</sub>	-	50	μA
Reverse Breakdown Voltage	V <sub>(BR)R</sub>	75	-	V
at I <sub>R</sub> = 100 μA	` '			
Total Capacitance	Ст	-	2	pF
at V <sub>R</sub> = 0 V, f = 1 MHz				'
Reverse Recovery Time at $I_F$ = 10 mA, $V_R$ = 6 V, $I_{rr}$ = 0.1 × $I_R$ , $R_L$ = 100 $\Omega$	t <sub>rr</sub>	-	4	ns



### **Electrical Characteristics Curves**

Fig 1. Power Derating Curve

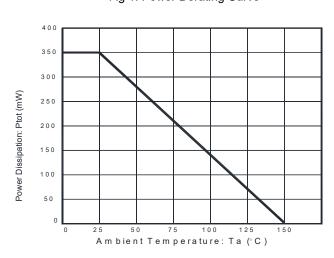


Fig 2. Forward Characteristics

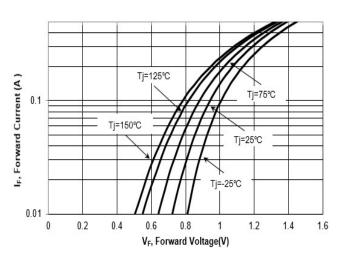


Fig 3. Reverse Current vs. Reverse Voltage

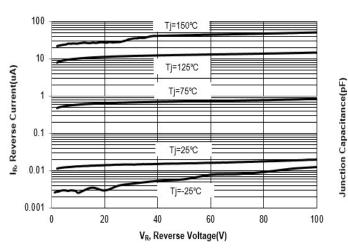
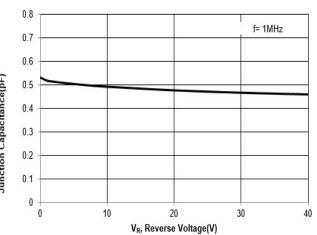


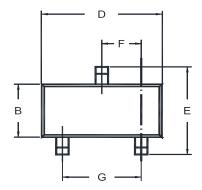
Fig 4. Total Capacitance vs. Reverse Voltage

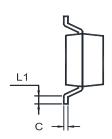


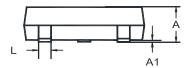


## Package Outline (Dimensions in mm)

**SOT-23** 

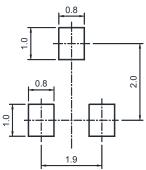






Unit	Α	A1	В	С	D	Е	F	G	L	L1
	1.20	0.100	1.40	0.19	3.04	2.6	1.02	2.04	0.51	0.2
mm	0.89	0.013	1.20	0.08	2.80	2.2	0.89	1.78	0.37	MIN

### **Recommended Soldering Footprint**



**Packing information** 

Tape Width		Pit	tch	Reel	Size		
Package	(mm)	mm	inch	mm	inch	Per Reel Packing Quantity	
SOT-23	8	4 ± 0.1	0.157 ± 0.004	178	7	3,000	

## **Marking information**

" A4 " = Part No.

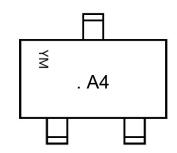
"•" = HAF (Halogen and Antimony Free)

"YM" = Date Code Marking

"Y" = Year

"M" = Month

Font type: Arial



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