# **BAS316WT-CH**

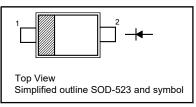
# Silicon Epitaxial Planar Switching Diode

#### **Features**

- AEC-Q101 Qualified
- Halogen and Antimony Free(HAF), RoHS compliant

#### **PINNING**

PIN	DESCRIPTION		
1	Cathode		
2	Anode		



Absolute Maximum Ratings (T<sub>a</sub> = 25 ∘C)

Parameter		Symbol	Value	Unit
Repetitive Peak Reverse Voltage	V <sub>RRM</sub>	100	V	
Reverse Voltage	V <sub>R</sub>	100	V	
Continuous Forward Current	I <sub>F</sub>	250	mA	
Repetitive Peak Forward Current	I <sub>FRM</sub>	500	mA	
Non-Repetitive Peak Forward Current	t = 1 µs t = 1 ms t = 1 s	I <sub>FSM</sub>	4 1 0.5	А
Power Dissipation $T_a = 25^{\circ}C$ $T_S \leq 90^{\circ}$		P <sub>D</sub>	150 500	mW
Junction Temperature		Tj	150	လိ
Storage Temperature Range	T <sub>stg</sub>	- 65 to + 150	္	

# **Thermal Characteristics**

Parameter	Symbol	Max.	Unit
Thermal Resistance from Junction to Ambient 1)	$R_{ heta JA}$	833	%C/W

 $<sup>^{1)}</sup>$  Device mounted on FR-4 PCB with minimum recommended pad layout.



# **BAS316WT-CH**

# Characteristics at T<sub>a</sub> = 25 ∘C

Parameter	Symbol	Max.	Unit
Forward Voltage at $I_F = 1 \text{ mA}$ at $I_F = 10 \text{ mA}$ at $I_F = 50 \text{ mA}$ at $I_F = 150 \text{ mA}$	V <sub>F</sub>	0.715 0.855 1 1.25	>
Reverse Current at $V_R$ = 25 V at $V_R$ = 75 V at $V_R$ = 25 V, $T_J$ = 150 °C at $V_R$ = 75 V, $T_J$ = 150 °C	I <sub>R</sub>	30 1 30 50	р Р Р Р Р Р Р
Diode Capacitance at V <sub>R</sub> = 0 V, f = 1 MHz	C <sub>tot</sub>	1.5	pF
Reverse Recovery Time at $I_F$ = 10 mA, $V_R$ = 6 V, $I_{rr}$ = 0.1 X $I_R$ , $R_L$ = 100 $\Omega$	t <sub>rr</sub>	4	ns



# **Electrical Characteristics Curves**

Fig 1. Power Derating Curve

Fig 2. Total Capacitance vs. Reverse Voltage

0.7

0.6

0.5

0.4

0.3

0.2

0.1

0

0

CT, Total Capacitance (PF)

Fig 3. Reverse Current vs. Reverse Voltage

T<sub>a</sub>, Ambient Temperature(°C)

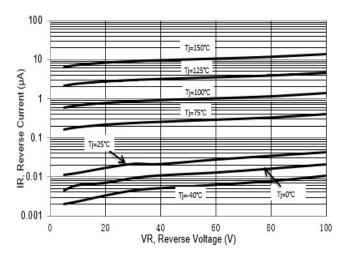


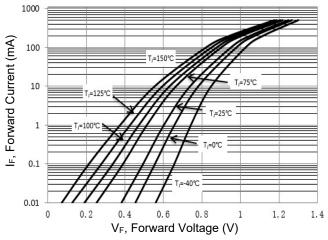
Fig 4. Forward Characteristics

4

VR, Reverse Voltage (V)

8

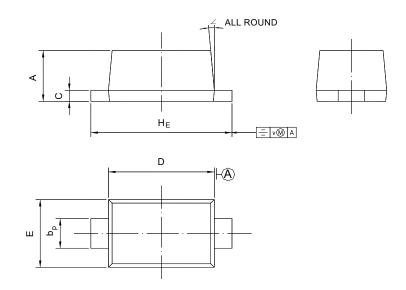
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#### **PACKAGE OUTLINE**

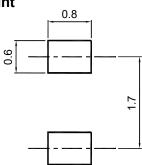
# Plastic surface mounted package; 2 leads

**SOD-523** 



UNIT	Α	bр	С	D	Е	H <sub>E</sub>	V		
mm	0.70 0.60		0.135 0.100	1.25 1.15	0.85 0.75	1.7 1.5	0.1	5°	

# **Recommended Soldering Footprint**



#### **Packing information**

Devleres	Tape Width	Pit	ch	Reel Size		Dan David Davidin in Oceantity	
Package	(mm)	mm	inch	mm	inch	Per Reel Packing Quantity	
SOD-523	8	4 ± 0.1	0.157 ± 0.004	178	7	4,000	

# **Marking information**

" III " = Cathode line

"•" = HAF (Halogen and Antimony Free)



" A " = Part No.

Font type: Arial

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