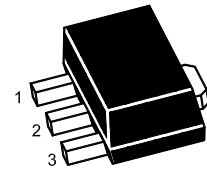


# 2SD1899U

## NPN Silicon Epitaxial Planar Transistor

### Features

- Low Collector Emitter Saturation Voltage
- Good  $h_{FE}$  linearity



1.Base 2.Collector 3.Emitter  
SOT-89 Plastic Package

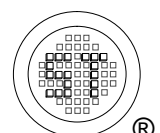
### Absolute Maximum Ratings ( $T_a = 25^\circ\text{C}$ )

Parameter	Symbol	Value	Unit
Collector Base Voltage	$V_{CBO}$	120	V
Collector Emitter Voltage	$V_{CEO}$	80	V
Emitter Base Voltage	$V_{EBO}$	5	V
Collector Current	$I_C$	1	A
Peak Collector Current, Pulsed	$I_{CM}$	2	A
Collector Power Dissipation	$P_{tot}$	0.5	W
Junction Temperature	$T_j$	150	$^\circ\text{C}$
Storage Temperature Range	$T_{stg}$	- 55 to + 150	$^\circ\text{C}$

### Thermal Characteristics

Parameter	Symbol	Max.	Unit
Thermal Resistance from Junction Ambient <sup>1)</sup>	$R_{\theta JA}$	250	$^\circ\text{C}/\text{W}$

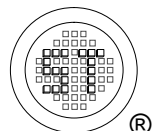
<sup>1)</sup> Device mounted on FR-4 substrate PC board, with minimum recommended pad layout.



# 2SD1899U

## Characteristics at $T_a = 25^\circ\text{C}$

Parameter	Symbol	Min.	Typ.	Max.	Unit	
DC Current Gain at $V_{CE} = 3\text{ V}$ , $I_C = 0.5\text{ A}$	Q	$h_{FE}$	120	-	270	-
	R	$h_{FE}$	180	-	390	-
Collector Base Cutoff Current at $V_{CB} = 100\text{ V}$	$I_{CBO}$	-	-	1	$\mu\text{A}$	
Emitter Base Cutoff Current at $V_{EB} = 4\text{ V}$	$I_{EBO}$	-	-	500	nA	
Collector Base Breakdown Voltage at $I_C = 50\text{ }\mu\text{A}$	$V_{(BR)CBO}$	120	-	-	V	
Collector Emitter Breakdown Voltage at $I_C = 1\text{ mA}$	$V_{(BR)CEO}$	80	-	-	V	
Emitter Base Breakdown Voltage at $I_E = 50\text{ }\mu\text{A}$	$V_{(BR)EBO}$	5	-	-	V	
Collector Emitter Saturation Voltage at $I_C = 500\text{ mA}$ , $I_B = 50\text{ mA}$	$V_{CE(sat)}$	-	-	400	mV	
Transition Frequency at $V_{CE} = 20\text{ V}$ , $I_C = 50\text{ mA}$ , $f = 100\text{ MHz}$	$f_T$	-	180	-	MHz	
Collector Base Capacitance at $V_{CB} = 10\text{ V}$ , $f = 1\text{ MHz}$	$C_{ob}$	-	9	-	pF	



## Electrical Characteristics Curves

Fig 1. DC Current Gain vs. Collector Current

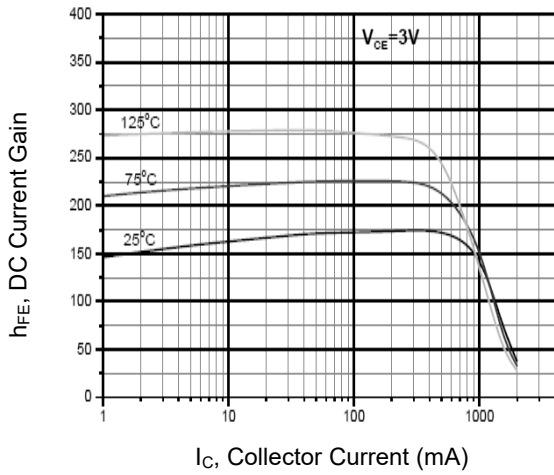


Fig 2.  $V_{CE(sat)}$  vs. Collector Current

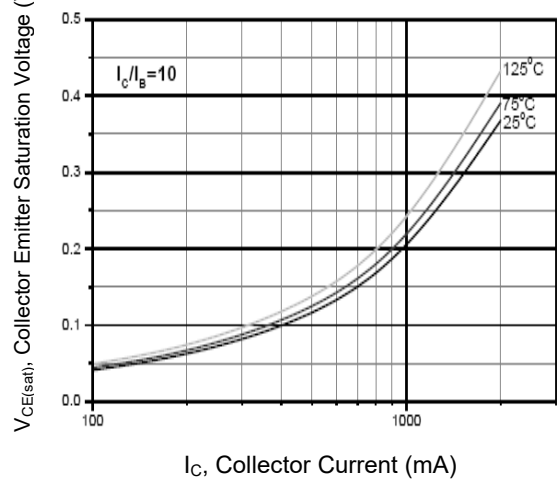


Fig 3.  $V_{BE(sat)}$  vs. Collector Current

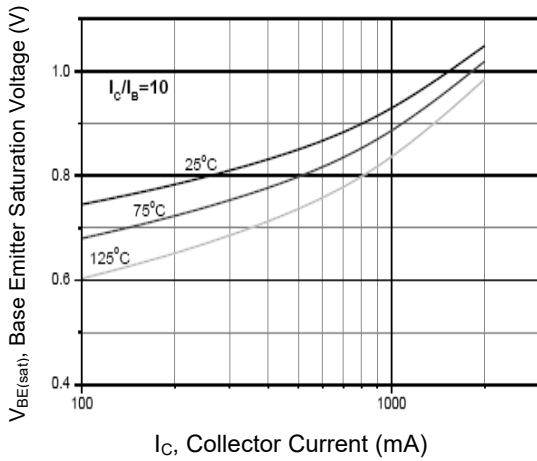
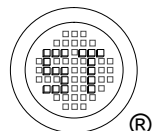
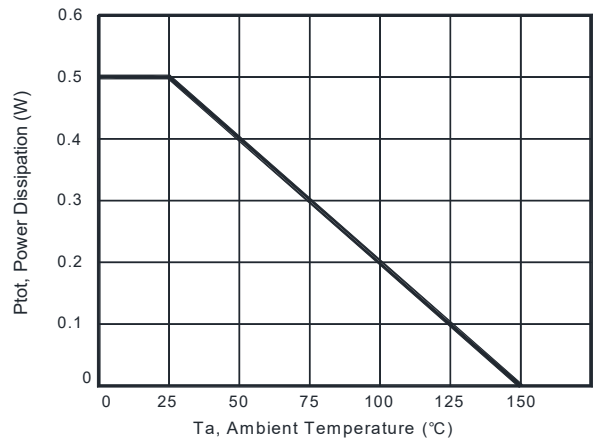


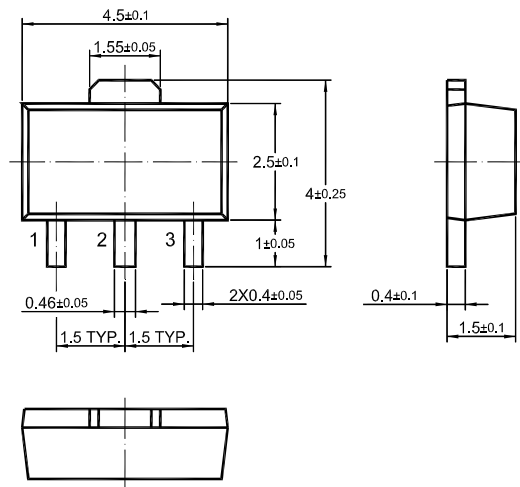
Fig 4. Power Derating Curve



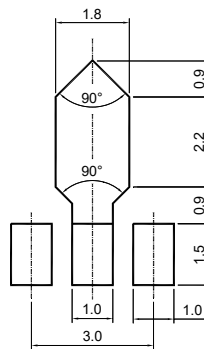
# 2SD1899U

## Package Outline (Dimensions in mm)

SOT-89



## Recommended Soldering Footprint



## Packing information

Package	Tape Width (mm)	Pitch		Reel Size		Per Reel Packing Quantity
		mm	inch	mm	inch	
SOT-89	12	$8 \pm 0.1$	$0.315 \pm 0.004$	178	7	1,000
				330	13	4,000

## Marking information

" 2SD1899\*U " = Part No. ("\*" = HFE grouping Code)

"YM" = Date Code Marking

"Y" = Year

"M" = Month

Font type: Arial

