

1SS400BP

Silicon Epitaxial Planar Switching Diode

Features

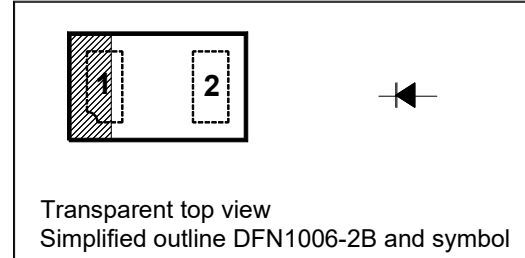
- Ultra small surface mounting type
- High reliability

Applications

- For high speed switching

PINNING

PIN	DESCRIPTION
1	Cathode
2	Anode

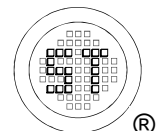


Absolute Maximum Ratings ($T_a = 25^\circ\text{C}$)

Parameter	Symbol	Value	Unit
Non-Repetitive Peak Reverse Voltage	V_{RM}	90	V
Reverse Voltage	V_R	80	V
Average Rectified Forward Current	$I_{F(AV)}$	100	mA
Peak Forward Current	I_{FM}	225	mA
Non-repetitive Peak Forward Surge Current(at $t = 1$ s)	I_{FSM}	500	mA
Junction Temperature	T_j	150	$^\circ\text{C}$
Storage Temperature Range	T_{stg}	- 55 to + 150	$^\circ\text{C}$

Characteristics at $T_a = 25^\circ\text{C}$

Parameter	Symbol	Min	Max.	Unit
Reverse Breakdown Voltage at $I_R = 10 \mu\text{A}$	$V_{(BR)R}$	80	-	V
Forward Voltage at $I_F = 100$ mA	V_F	-	1.2	V
Reverse Current at $V_R = 80$ V	I_R	-	0.1	μA
Capacitance Between Terminals at $V_R = 0.5$ V, $f = 1$ MHz	C_T	-	3	pF
Reverse Recovery Time at $I_F = 10$ mA, $I_{rr} = 0.1 \times I_R$, $V_R = 6$ V, $R_L = 100 \Omega$	t_{rr}	-	4	ns



Electrical Characteristics Curves

Fig 1. Reverse Current vs. Reverse Voltage

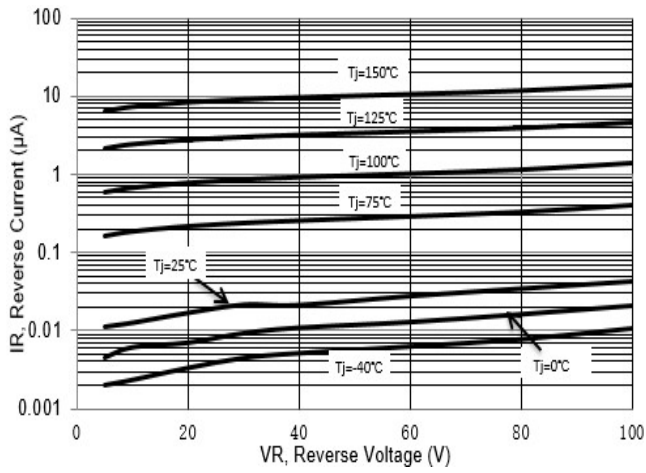


Fig 2. Forward Characteristics

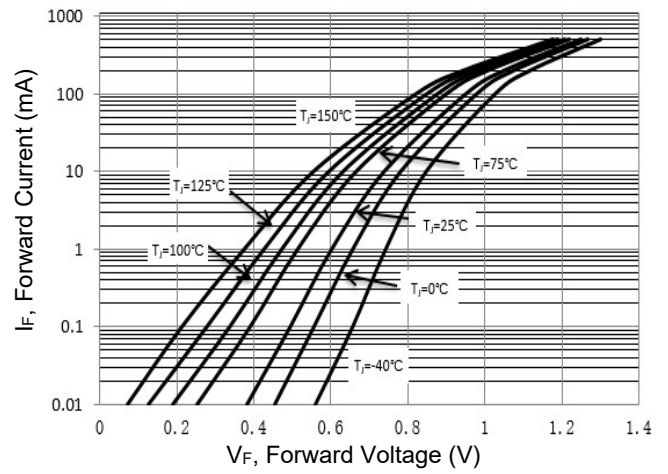
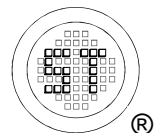
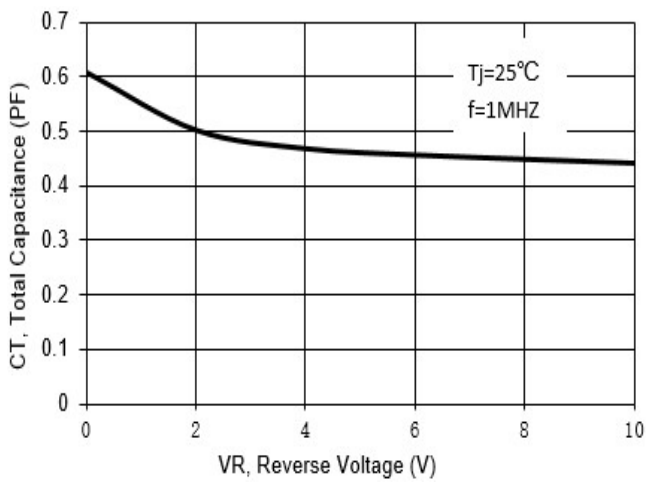


Fig 3. Total Capacitance vs. Reverse Voltage

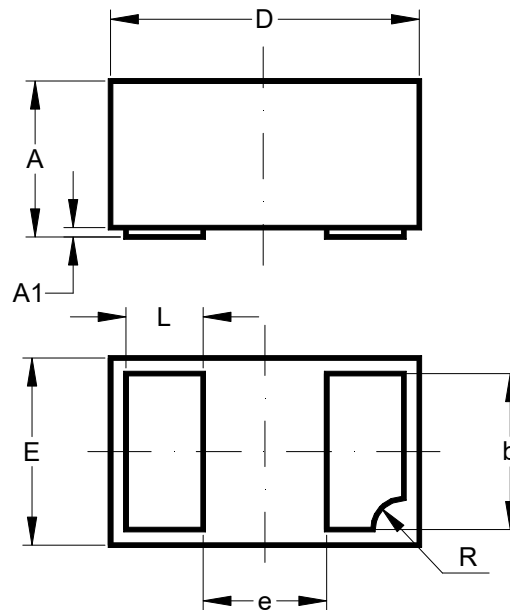


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PACKAGE OUTLINE

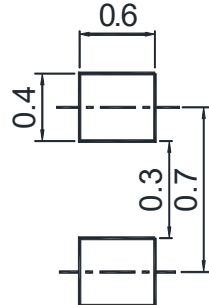
Plastic surface mounted package; 2 leads

DFN1006-2B



UNIT	A	A1	b	D	E	e	L	R
mm	0.40 0.36	0.05 0	0.55 0.45	1.05 0.95	0.65 0.55	0.4	0.3 0.2	0.15 0.05

Recommended Soldering Footprint



Packing information

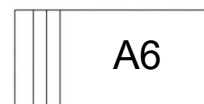
Package	Tape Width (mm)	Pitch		Reel Size		Per Reel Packing Quantity
		mm	inch	mm	inch	
DFN1006-2B	8	2 ± 0.1	0.079 ± 0.004	178	7	10,000

Marking information

" A6 " = Part No.

" III " = Cathode Line

Font type: Arial



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